

# Complete Stability of Neural Networks With Extended Memristors

Mauro Di Marco<sup>1b</sup>, Mauro Forti<sup>1b</sup>, Riccardo Moretti<sup>1b</sup>, Luca Pancioni, and Alberto Tesi<sup>1b</sup>

**Abstract**—The article considers a large class of delayed neural networks (NNs) with extended memristors obeying the Stanford model. This is a widely used and popular model that accurately describes the switching dynamics of real nonvolatile memristor devices implemented in nanotechnology. The article studies via the Lyapunov method complete stability (CS), i.e., convergence of trajectories in the presence of multiple equilibrium points (EPs), for delayed NNs with Stanford memristors. The obtained conditions for CS are robust with respect to variations of the interconnections and they hold for any value of the concentrated delay. Moreover, they can be checked either numerically, via a linear matrix inequality (LMI), or analytically, via the concept of Lyapunov diagonally stable (LDS) matrices. The conditions ensure that at the end of the transient capacitor voltages and NN power vanish. In turn, this leads to advantages in terms of power consumption. This notwithstanding, the nonvolatile memristors can retain the result of computation in accordance with the in-memory computing principle. The results are verified and illustrated via numerical simulations. From a methodological viewpoint, the article faces new challenges to prove CS since due to the presence of nonvolatile memristors the NNs possess a continuum of nonisolated EPs. Also, for physical reasons, the memristor state variables are constrained to lie in some given intervals so that the dynamics of the NNs need to be modeled via a class of differential inclusions named differential variational inequalities.

**Index Terms**—Complete stability (CS), differential variational inequalities, in-memory computing, linear matrix inequality (LMI), Lyapunov diagonally stable (LDS) matrices, Lyapunov method, memristor, neural networks (NNs).

## I. INTRODUCTION

TRADITIONAL computers based on Von Neumann architectures are currently facing severe challenges to process big amount of data in the Internet of Things and edge computing systems [1], [2], [3], [4]. These are due to the Moore’s law slowdown and the memory wall bottleneck, i.e., the difficulties to continuously transmit huge amount of data between the central processing unit (CPU) and the memory (e.g., the RAM) located at different sites. The use of emerging nanoscale devices as memristors is a long-term vision aimed

at overcoming the limitations of Von Neumann architectures via the implementation of innovative analog and parallel neuromorphic computing paradigms [5], [6], [7]. Especially, nonvolatile memristors can handle their state for a long time and thus they enable to circumvent the memory bottleneck according to the in-memory computing principle [8], [9], [10]. Namely, the same memristor device has a key role in the computation but also in memorizing the result of computation, which is the same mechanism at the core of a biological brain.

The memristor was envisioned by Chua in the seminal 1971 article [11] as the fourth basic passive circuit element together with the resistor, capacitor, and inductor. An *ideal* voltage-controlled memristor is a circuit element defined by a nonlinear relation between flux (the integral of voltage) and charge (the integral of current). In the voltage-current domain it satisfies a state-dependent Ohm’s law where the flux has the role of the state variable. The main limitation of ideal memristors is that they are unable to satisfactorily model real memristor devices in nanotechnology [12], [13], [14], [15], [16], [17]. Actually, the most effective way to accurately describe real devices is via more complex models named *extended* memristors [18],<sup>1</sup> which involve more general forms of the quasi-static Ohm’s law and also additional internal state variables such as geometric features of the device or the temperature. One of the most popular and widely used real memristor models is the Stanford one [16], [20], [21], [22]. Its importance is due to the fact that it is able to accurately describe the hysteresis loops and nonlinear dynamics displayed by a broad class of filamentary resistive random access memory (RRAM) devices exhibiting bipolar switching characteristics (more details in Section II).

Following the definition in the seminal papers [23], [24], [25], and [26], by *complete stability* (CS) of a neural network (NN) it is meant that any trajectory converges toward an equilibrium point (EP) as  $t \rightarrow \infty$ . In view of the applications, an extremely important case of CS is that where the NN has *multiple EPs* and each solution tends to an EP depending upon the initial condition. In fact, in this case a NN is tailor made to implement content addressable memories (CAMs), where the stable EPs correspond to the memorized patterns, to solve combinatorial optimization problems, where the stable states are the local minima of the cost function, and to solve in real time several other tasks in the fields of image processing and adaptive signal processing [26], [27], [28]. The physical

Manuscript received 18 August 2022; revised 31 March 2023; accepted 18 May 2023. Date of publication 6 June 2023; date of current version 8 October 2024. This work was supported in part by the Ministero dell’Istruzione, dell’Università e della Ricerca (MIUR), under Contract 2017LSCR4K-003. (Corresponding author: Mauro Forti.)

Mauro Di Marco, Mauro Forti, Riccardo Moretti, and Luca Pancioni are with the Department of Information Engineering and Mathematics, University of Siena, 53100 Siena, Italy (e-mail: dimarco@dii.unisi.it; forti@diism.unisi.it; moretti@diism.unisi.it; pancioni@diism.unisi.it).

Alberto Tesi is with the Department of Information Engineering, University of Florence, 50139 Florence, Italy (e-mail: alberto.tesi@unifi.it).

Digital Object Identifier 10.1109/TNNLS.2023.3279406

<sup>1</sup>A detailed discussion on the nomenclature, genealogy, and classification of memristors is available in [18] and [19].

mechanism of static pattern formation, i.e., the emergence in the long run of some stationary voltage distribution for the neurons, is also inherently related to CS [29]. Adding delays in the NN model further extends the application fields, since a completely stable delayed NN is well suited also to solve motion-related problems [30]. We refer the reader to [31] for a thorough survey of the main recent results in the literature on CS of NNs. It is worth mentioning that in the special case where there exists a *unique EP*, CS reduces to global asymptotic stability (GAS) [32]. However, GAS NNs will not be considered in this article.

CS of NNs without memristors has been widely investigated in the literature using the Lyapunov method, the dichotomy of omega limit sets for cooperative systems, the global consistency of decision schemes for competitive systems and the Łojasiewicz inequality and the concept of trajectories with finite length, see, e.g., [23], [24], [31], [33], [34], [35], [36], [37], and references therein. Quite on the contrary, the study of CS of memristor NNs (MNNs) is an important topic that is still in its infancy, as confirmed by the limited number of available studies. In [38], [39], and [40], CS of MNNs with ideal memristors has been investigated in the case of symmetric interconnections and in that of cooperative (positive) interconnections between neurons, using a technique referred to as flux-charge analysis method [19]. This method is used also in [41] and [42] for multistability of delayed cellular NNs with ideal memristors. Moreover, there are contributions on CS of NNs where memristors are modeled as elements switching between two values of the resistance displayed by the pinched hysteresis loop [43], [44]. We also mention the related relevant papers [45], [46] and [47] dealing with techniques for synchronization of various classes of NNs with delay and switching memristors. However, how to employ such switching models to describe real memristor devices has not been studied so far. Finally, [48] deals with CS of a class of MNNs where memristors obey the Threshold Adaptive Memristor (TEAM) model and [49] studies CS of a class of cellular MNNs with bistable neurons.

The previous discussion shows that there is a substantial lack of results on CS for NNs with general and reliable real memristor models. In turn, this represents a serious limitation in view of the practical implementation in nanotechnology of MNNs. Goal of this manuscript is to make a first step to fill this relevant gap. Indeed, in this article, we study for the first time CS of a large class of MNNs with nonvolatile extended memristors obeying the widely used and popular Stanford model. For generality, we also allow for the presence of concentrated delays in neuron interconnections. We will refer to these NNs as Stanford MNNs (SMNNs).

Due to the peculiar mathematical structure of the SMNNs equations, there are a number of challenges and mathematical difficulties to analyze CS as follows.

- 1) Each neuron is described by a third-order nonlinear dynamical system since, in addition to the capacitor voltage, there are two more state variables given by the length  $g$  of the gap of the memristor insulating material and the temperature  $T$  of the filament tip. Therefore,

for  $n$  neurons, an SMNN is described by a set of  $3n$  coupled differential equations. This is remarkably more complex than in a traditional memristor-less NN, or in MNNs with ideal memristors, which can be described by a set of  $n$  differential equations.

- 2) Due to the nonvolatility of the memristors, it follows that for structural reasons an SMNN has a continuum of nonisolated EPs, a new scenario calling for peculiar methods to address CS.
- 3) Due to physical limitations, one state variable of each neuron, i.e., the gap  $g$ , is constrained to lie in a given interval. Such hard constraints are mathematically described by a class of differential inclusions termed differential variational inequalities (DVI) [50]. Their study thus needs nonstandard tools from nonsmooth analysis.

The main results in the article can be summarized as follows.

- 1) We provide sets of conditions ensuring CS of the considered class of MNNs. These hold for any concentrated delay and they are robust with respect to variations of the neuron interconnections.
- 2) The conditions for CS can be effectively tested numerically since they are expressed in the form of linear matrix inequalities (LMIs) [51]. For several relevant classes of interconnection matrices they can also be checked analytically via the concept of Lyapunov diagonally stable (LDS) matrices [52].
- 3) The conditions for CS guarantee that at the end of the transient the capacitor voltages vanish, hence power consumption of the SMNNs drops off at a steady state. This is a desirable feature for NNs used in the Internet of Things or edge computing systems [3], [4], where power efficiency is crucial. It is stressed that, although capacitor voltages vanish, the nonvolatile memristors can retain in memory the result of the SMNN computation, i.e., the asymptotic values of the memristors gaps, in accordance with the in-memory computing principle.

The article is organized as follows. In the remaining part of this section we give the notation and some preliminary mathematical results. Then, Section II discusses the Stanford memristor and SMNN models considered in the article. The main results on CS of SMNNs are given in Section III, while Section IV provides numerical simulations illustrating the applicability of the results on CS. Finally, Section V draws the main conclusions of the article.

*Notation:* If  $x, y \in \mathbb{R}^n$  are column vectors,  $\langle x, y \rangle = \sum_{i=1}^n x_i y_i$  is the scalar product, while  $\|x\| = \langle x, x \rangle^{1/2} = (\sum_{i=1}^n x_i^2)^{1/2}$  denotes the Euclidean norm of  $x$  and  $\|x\|_\infty = \max_{i=1, \dots, n} |x_i|$  is the infinity norm. Given  $x \in \mathbb{R}^n$  and set  $Q \subset \mathbb{R}^n$ , we let  $\text{dist}(x, Q) = \inf_{y \in Q} \|x - y\|$ . If  $A = A^\top \in \mathbb{R}^{n \times n}$ , where superscript  $\top$  means the transpose, is a symmetric square matrix,  $\Lambda_m(A)$  (resp.,  $\Lambda_M(A)$ ) is the minimum eigenvalue (resp., maximum eigenvalue) of  $A$ . Moreover,  $A > 0$  (resp.  $A \geq 0$ ) means that  $A$  is positive definite (resp. positive semidefinite). If  $A \in \mathbb{R}^{n \times n}$  is (in general) nonsymmetric,  $\|A\|_2 = [\Lambda_M(A^\top A)]^{1/2}$  is the induced

two-norm of  $A$ . We denote by  $C([-\tau, 0], \mathbb{R}^n)$  the space of continuous functions from  $[-\tau, 0]$  to  $\mathbb{R}^n$ , where  $\tau > 0$ .

### A. Preliminaries

1) *Tangent and Normal Cones*: We recall some properties of tangent and normal cones that are useful in the article. The reader is referred to [50] for more details.

Let  $Q \subset \mathbb{R}^n$  be a nonempty closed convex set. The tangent cone to  $Q$  at  $x \in Q$  is defined as

$$T_Q(x) = \left\{ z \in \mathbb{R}^n : \liminf_{\zeta \rightarrow 0^+} \frac{\text{dist}(x + \zeta z, Q)}{\zeta} = 0 \right\}$$

while the normal cone to  $Q$  at  $x \in Q$  is given by

$$N_Q(x) = \{ w \in \mathbb{R}^n : \langle w, z \rangle \leq 0 \quad \forall z \in T_Q(x) \}.$$

It can be shown that, for any  $x \in Q$ ,  $T_Q(x)$  and  $N_Q(x)$  are nonempty closed convex cones in  $\mathbb{R}^n$ . Furthermore,  $N_Q(\cdot)$  is a monotone operator, i.e., for any  $x, y \in Q$  and any  $n_x \in N_Q(x)$ ,  $n_y \in N_Q(y)$ , we have  $\langle x - y, n_x - n_y \rangle \geq 0$ .

2) *Differential Variational Inequalities*: A DVI is a particular class of differential inclusion that is especially useful to model the dynamics of systems evolving in a closed convex subset of  $\mathbb{R}^n$  defined for instance by some hard constraints. Next, we recall the definition and some basic properties of DVIs. For a more thorough treatment the reader is referred to [50, Ch. 5].

*Definition 1* [50, pp. 265]: Let  $Q \subset \mathbb{R}^n$  be a nonempty closed convex set and  $F : Q \rightarrow \mathbb{R}^n$ . A DVI is a problem of the form: find an absolutely continuous function  $x(t)$ ,  $t \in [t_1, t_2]$ , such that  $x(t) \in Q$  for all  $t \in [t_1, t_2]$  and

$$\dot{x}(t) \in F(x(t)) - N_Q(x(t)) \quad (1)$$

for almost all (a.a.)  $t \in [t_1, t_2]$ .

*Property 1* [50, Th. 1, pp. 267]: Let  $Q \subset \mathbb{R}^n$  be a nonempty compact convex set. Also, suppose that  $F : Q \rightarrow \mathbb{R}^n$  is continuous in  $Q$ . Then, for any initial condition (IC)  $x_0 \in Q$ , the DVI (1) has at least a solution  $x(t)$ , such that  $x(0) = x_0$ , which is defined for  $t \geq 0$ .

3) *Relevant Classes of Matrices*: In the article we will consider these classes of matrices  $A \in \mathbb{R}^{n \times n}$  [53].

Matrix  $A \in \mathcal{P}$  if all its principal minors are positive.

Matrix  $A$  is said to be (positive) LDS if there exists a diagonal matrix  $D \in \mathbb{R}^{n \times n}$ ,  $D > 0$ , such that  $DA + A^\top D > 0$ . If  $A$  is LDS then  $A \in \mathcal{P}$ . The converse, however does not hold in general.

Matrix  $A$  is an  $M$ -matrix if  $a_{ij} \leq 0$  for any  $i \neq j$  and  $A \in \mathcal{P}$ , while  $A$  is an  $H$ -matrix if the comparison matrix of  $A$ , i.e.,

$$M(A) = \begin{cases} |a_{ii}|, & i = j \\ -|a_{ij}|, & i \neq j \end{cases}$$

for  $i, j = 1, \dots, n$ , is an  $M$ -matrix.

More details about  $\mathcal{P}$  and LDS matrices and their relationships, and a discussion of their importance in the stability analysis of NNs and interconnected dynamical systems, can be found in [52].

## II. SMNN MODEL

### A. Stanford Memristor Model

Let  $v(t)$  [resp.,  $i(t)$ ] be the voltage (resp., the current) in a memristor. Moreover, define the flux  $\phi(t) = \int_{-\infty}^t v(\sigma) d\sigma$  and charge  $q(t) = \int_{-\infty}^t i(\sigma) d\sigma$ . An ideal flux-controlled memristor is defined by the constitutive relation  $q = \hat{q}(\phi)$  [11], where  $\hat{q} : \mathbb{R} \rightarrow \mathbb{R}$  is a nonlinear  $C^1$  function. By differentiating in time, the memristor satisfies the quasi-static Ohm's law

$$i = \dot{q} = \hat{q}'(\phi) \frac{d\phi}{dt} = G_{\text{id}}(\phi) v \quad (2)$$

$$\dot{\phi} = v \quad (3)$$

where  $G_{\text{id}}(\phi) \doteq \hat{q}'(\phi)$  has dimension of  $\text{Ohm}^{-1}$  and is named (state dependent) memductance. The state variable is the flux  $\phi$ . When subject to a zero-mean sinusoidal voltage, an ideal memristor displays its main fingerprint consisting in a pinched hysteresis loop in the  $v - i$  plane.

An extended memristor satisfies [18], [19]

$$i = G_{\text{ext}}(x, v) v \quad (4)$$

$$\dot{x} = F_{\text{ext}}(x, v) \quad (5)$$

where  $x \in \mathbb{R}^n$  is a vector of state variables,  $G_{\text{ext}}(\cdot, \cdot) : \mathbb{R}^n \times \mathbb{R} \rightarrow \mathbb{R}$  is the memductance and  $F_{\text{ext}}(\cdot, \cdot) : \mathbb{R}^n \times \mathbb{R} \rightarrow \mathbb{R}^n$ . It is further assumed that  $G_{\text{ext}}(\cdot, \cdot)$  is bounded in a neighborhood of  $(x, 0)$  for any  $x \in \mathbb{R}^n$ .

Clearly, an extended memristor has a more general form of quasi-static Ohm's law with respect to an ideal memristor. Moreover, it may have more than one internal state variable as defined by the state vector  $x$ . Due to (4), an extended memristor still displays a pinched hysteresis loop in the  $v - i$  plane when subject to a zero-mean sinusoidal voltage.

We consider the popular and widely used Stanford memristor model [16], [20], [21], [22]

$$i = I_0 \exp\left(-\frac{g}{\tilde{g}}\right) \sinh\left(\frac{v}{V_0}\right) \quad (6)$$

$$\dot{g} = -v_0 \exp\left(-\frac{q_e E}{KT}\right) \sinh\left(\frac{q_e a_0 \psi}{\ell KT} v\right) \quad (7)$$

$$\dot{T} = \frac{vi}{C_{\text{th}}} - \frac{T - T_{\text{amb}}}{\Theta_{\text{th}}} \quad (8)$$

which accurately describes the switching mechanism and dynamics due to the formation/rupture of conductive filaments in a broad class of filamentary RRAM devices exhibiting bipolar switching characteristics.

The Stanford memristor state variables are  $x = (g, T)^\top \in \mathbb{R}^2$ , where the gap  $g$  is the distance between the top electrode and the tip of the conductive filament and  $T$  is the temperature of the filament tip. The model is characterized by a number of positive parameters. We refer to Table I for the nomenclature and for specific parameter values of some real memristor devices implemented in nanotechnology [54]. One first set of parameters  $a_0, E, \ell, T_{\text{amb}}, \Theta_{\text{th}}$  and  $C_{\text{th}}$  are named 'process parameters' since they depend upon the fabrication aspects as device structure and material properties and measurement setup. The second set  $I_0, \tilde{g}, V_0, v_0$  and  $\psi$  are named 'switching parameters' since they mainly describe the filament evolution (gap dynamics) and the hysteresis loops in response to periodic

TABLE I  
STANFORD MEMRISTOR PARAMETERS AND TYPICAL VALUES FOR SOME REAL DEVICES

Symbol	Unit	Value	Description
$a_0$	m	$0.25 \times 10^{-9}$	Atomic hopping distance
$E$	eV	1.25	Activation energy for vacancy generation/recombination
$\ell$	m	$5.0 \times 10^{-9}$	Oxide thickness
$g_m$	m	$0.6 \times 10^{-9}$	Minimum gap distance
$g_M$	m	$1.1 \times 10^{-9}$	Maximum gap distance
$I_0$	A	$2.0 \times 10^{-3}$	$i-v$ fitting parameter
$\tilde{g}$	m	$4.0 \times 10^{-10}$	=
$V_0$	V	0.45	=
$v_0$	m/s	500	Gap dynamics fitting parameter
$\psi_0$	-	16.5	=
$T_{\text{amb}}$	K	298	Ambient temperature
$C_{\text{th}}$	J/K	$3.18 \times 10^{-16}$	Effective thermal capacitance
$\Theta_{\text{th}}$	s	$2.3 \times 10^{-10}$	Effective thermal time constant

inputs. Additionally,  $K = 1.38 \times 10^{-23}$  J/K is the Boltzmann's constant and  $q_e = 1.6 \times 10^{-19}$  C is the elementary unit charge. We refer the reader to [20], [21], [22], and [16] for a detailed discussion on the physical mechanisms described by (6)–(8).

Due to physical constraints, the filament gap  $g$  has a lower and an upper bound [16]

$$g \in [g_m, g_M].$$

From a mathematical viewpoint, the best way to impose such hard constraint is to replace (7) with the following DVI [50]

$$\dot{g} \in -v_0 \exp\left(-\frac{q_e E}{KT}\right) \sinh\left(\frac{q_e a_0 \psi}{\ell KT} v\right) - N_{[g_m, g_M]}(g) \quad (9)$$

where  $N_{[g_m, g_M]}(g)$ , the normal cone to  $[g_m, g_M]$  at  $g$ , ensures that  $g$  evolves within  $[g_m, g_M]$  for all times.

Henceforth, we suppose without loss of generality that  $T(0) \geq T_{\text{amb}}$ . Note that in (8) the term  $v(t)i(t) \geq 0$  for all  $t$ . It easily follows that  $T(0) \geq T_{\text{amb}}$  implies  $T(t) \geq T_{\text{amb}}$  for  $t \geq 0$ . Note that if power is turned off, i.e., we let  $v = 0$  and  $T = T_{\text{amb}}$  (the ambient temperature), then we have  $\dot{g} = 0$  and  $\dot{T} = 0$ , i.e., any point of the form

$$(\bar{g}, T_{\text{amb}}), \bar{g} \in [g_m, g_M] \quad (10)$$

is an EP of (6)–(8). This implies that Stanford model is nonvolatile since it is able to retain in memory a continuum of nonisolated EPs as given in (10). Finally, note that Stanford model can be put in the form of an extended memristor by defining its memductance as  $G_{\text{ext}}(v, g) = I_0 e^{-g/\tilde{g}} \sinh(v/V_0)/v$  for  $v \neq 0$  and  $G_{\text{ext}}(v, g) = (I_0/V_0)e^{-g/\tilde{g}}$  if  $v = 0$ .

### B. SMNN Equations

We consider a class of delayed MNNs with an additive interconnection structure where each neuron has a capacitor  $C$  and a memristor satisfying Stanford model. The SMNNs obey the system of delayed DVIs

$$C\dot{v}_i(t) = -I_0 \exp\left(-\frac{g_i(t)}{\tilde{g}}\right) \sinh\left(\frac{v_i(t)}{V_0}\right)$$

$$+ \sum_{j=1}^n a_{ij} f(v_j(t)) + \sum_{j=1}^n a_{ij}^{\tau} f(v_j(t-\tau)) \quad (11)$$

$$\dot{g}_i(t) \in -v_0 \exp\left(-\frac{q_e E}{KT_i(t)}\right) \sinh\left(\frac{q_e a_0 \psi}{\ell KT_i(t)} v_i(t)\right) - N_{[g_m, g_M]}(g_i(t)) \quad (12)$$

$$\dot{T}_i(t) = \frac{I_0 \exp\left(-\frac{g_i(t)}{\tilde{g}}\right) v_i(t) \sinh\left(\frac{v_i(t)}{V_0}\right)}{C_{\text{th}}} - \frac{T_i(t) - T_{\text{amb}}}{\Theta_{\text{th}}} \quad (13)$$

for  $i = 1, \dots, n$ , where  $a_{ij}$  (resp.,  $a_{ij}^{\tau}$ ),  $i, j = 1, \dots, n$ , are the neuron interconnections (resp., delayed neuron interconnections) and  $0 < \tau < +\infty$  is a constant concentrated delay.

Moreover, the neuron activation  $f: \mathbb{R} \rightarrow \mathbb{R}$  is such that  $f(0) = 0$ , it is Lipschitz continuous, i.e.,  $|f(\zeta_1) - f(\zeta_2)| \leq L_f |\zeta_1 - \zeta_2|$  for any  $\zeta_1, \zeta_2 \in \mathbb{R}$ , for some  $0 < L_f < +\infty$  and it satisfies the following condition.

*Assumption 1:* For any  $V > 0$  there exists  $\gamma(V)$  such that

$$0 < \gamma(V) \leq \frac{f(v_i)}{v_i} \leq 1 \quad (14)$$

for all  $v_i \in [-V, V]$ ,  $v_i \neq 0$ , and  $i = 1, \dots, n$ .

Several widely used neuron activations satisfy Assumption 1. For example, the piecewise linear activation  $f(\zeta) = (1/2)(|\zeta + 1| - |\zeta - 1|)$  of standard cellular NNs [26] satisfies this assumption with  $\gamma(V) = \max\{1, 1/V\}$ , while the sigmoidal activation  $f(\zeta) = (2/\pi) \arctan(\pi \zeta/2)$  of Hopfield NNs [55] satisfies the same assumption with  $\gamma(V) = 1/V$ . Moreover, note that there are unbounded activations that satisfy Assumption 1, as for instance  $f(\zeta) = \zeta$  if  $\zeta \geq 0$ ,  $f(\zeta) = (1/2)\zeta$  if  $\zeta < 0$ .

Note that (11)–(13) are  $3n$  coupled differential equations in  $3n$  state variables, i.e., the  $n$  capacitor voltages  $v_i$ , the  $n$  memristor gaps  $g_i$  and the  $n$  memristor temperatures  $T_i$ .

### C. Existence and Uniqueness of the Solution

Since a SMNN obeys a class of differential inclusions defined by a discontinuous and multivalued vector field, first of all, we have to discuss what is meant by a solution of an initial value problem (IVP) associated with the SMNN.

Let  $v = (v_1, \dots, v_n)^{\top}$ ,  $g = (g_1, \dots, g_n)^{\top}$ ,  $T = (T_1, \dots, T_n)^{\top}$ ,  $\tilde{T}_{\text{amb}} = (T_{\text{amb}}, \dots, T_{\text{amb}})^{\top} \in \mathbb{R}^n$ ,  $A =$



$[a_{ij}]$ ,  $A^\tau = [a_{ij}^\tau] \in \mathbb{R}^{n \times n}$ .<sup>2</sup> Moreover, introduce the following diagonal mappings:

- 1)  $\mathfrak{F}(v) = (f(v_1), \dots, f(v_n))^\top : \mathbb{R}^n \rightarrow \mathbb{R}^n$
- 2)  $\mathfrak{D}(v, g) = (\mathfrak{d}(v_1, g_1), \dots, \mathfrak{d}(v_n, g_n))^\top : \mathbb{R}^{2n} \rightarrow \mathbb{R}^n$ ,  
where for  $i = 1, \dots, n$

$$\mathfrak{d}(v_i, g_i) = I_0 \exp\left(-\frac{g_i}{\bar{g}}\right) \sinh\left(\frac{v_i}{V_0}\right). \quad (15)$$

- 3)  $\mathfrak{G}(v, T) = (\mathfrak{g}(v_1, T_1), \dots, \mathfrak{g}(v_n, T_n))^\top : \mathbb{R}^{2n} \rightarrow \mathbb{R}^n$ ,  
where for  $i = 1, \dots, n$

$$\mathfrak{g}(v_i, T_i) = -v_0 \exp\left(-\frac{q_e E}{K T_i}\right) \sinh\left(\frac{q_e a_0 \psi}{\ell K T_i} v_i\right). \quad (16)$$

- 4)  $\mathfrak{T}(v, g) = (\mathfrak{t}(v_1, g_1), \dots, \mathfrak{t}(v_n, g_n))^\top : \mathbb{R}^{2n} \rightarrow \mathbb{R}^n$ ,  
where for  $i = 1, \dots, n$

$$\mathfrak{t}(v_i, g_i) = \frac{I_0 \exp\left(-\frac{g_i}{\bar{g}}\right) v_i \sinh\left(\frac{v_i}{V_0}\right)}{C_{th}}. \quad (17)$$

Finally, let

$$D_m \doteq \frac{I_0}{V_0} \text{diag}\left(\exp\left(-\frac{g_M}{\bar{g}}\right), \dots, \exp\left(-\frac{g_M}{\bar{g}}\right)\right) \in \mathbb{R}^{n \times n}. \quad (18)$$

Note that the diagonal elements of  $D_m$  are equal to the minimum value attained by the small signal memductance  $(I_0/V_0)e^{-g_i/\bar{g}}$  when  $g_i \in [g_m, g_M]$ ,  $i = 1, \dots, n$ .

Then, we can rewrite the SMNN equations as

$$C \dot{v}(t) = -\mathfrak{D}(v(t), g(t)) + A \mathfrak{F}(v(t)) + A^\tau \mathfrak{F}(v(t - \tau)) \quad (19)$$

$$\dot{g}(t) \in \mathfrak{G}(v(t), T(t)) - N_\Gamma(g(t)) \quad (20)$$

$$\dot{T}(t) \in \mathfrak{T}(v(t), g(t)) - \frac{T(t) - \tilde{T}_{amb}}{\Theta_{th}} \quad (21)$$

where

$$\Gamma \doteq [g_m, g_M]^n \subset \mathbb{R}^n.$$

**Definition 2 (IVP):** Consider function  $v \in C^0([-\tau, 0], \mathbb{R}^n)$  and vectors  $g_0, T_0 \in \mathbb{R}^n$  such that  $g_{0,i} \in [g_m, g_M]$  and  $T_{0,i} \geq T_{amb}$ ,  $i = 1, \dots, n$ . By a solution in  $[-\tau, \tilde{t}]$  of the SMNN with ICs  $v, g_0, T_0$ , we mean a function  $(v, g, T) : [-\tau, \tilde{t}] \rightarrow \mathbb{R}^{3n}$ , such that:

- 1)  $(v, g, T)$  is continuous in  $[-\tau, \tilde{t}]$  and absolutely continuous in  $[0, \tilde{t}]$ ;
- 2) we have  $v(t) = v(t), g(t) = g_0, T(t) = T_0, t \in [-\tau, 0]$ ;
- 3)  $(v, g, T)$  satisfies (19)–(21) for a.a.  $t \in [0, \tilde{t}]$ ; and
- 4) we have  $g(t) \in \Gamma$  for all  $t \in [0, \tilde{t}]$ .

An EP of the SMNN is a constant solution  $(\bar{v}, \bar{g}, \bar{T})$ ,  $t \geq -\tau$ . It can be easily checked that any point in  $\mathbb{R}^{3n}$  such that  $\bar{v}_i = 0$ ,  $\bar{g}_i \in [g_m, g_M]$  and  $\bar{T}_i = T_{amb}$ ,  $i = 1, \dots, n$ , is an EP. Then, a SMNN has a continuum of nonisolated EPs.

Let

$$v_M \doteq \max\left\{1, V_0 \sqrt{6 \left(\frac{V_0}{I_0} \exp\left(\frac{g_M}{\bar{g}}\right) (M_F + \varepsilon) - 1\right)}\right\} \quad (22)$$

<sup>2</sup>Note that, with some abuse of notation, henceforth we use  $v, g$  and  $T$  to denote the vectors of voltages, gaps and temperatures, while in (6)–(8) the same notations are used for the scalar quantities given by the voltage, gap, and temperature of a single memristor.

$$T_M \doteq T_{amb} + \Theta_{th} \left( I_0 v_M \exp\left(-\frac{g_m}{\bar{g}}\right) \sinh\left(\frac{v_M}{V_0}\right) + \varepsilon \right) \quad (23)$$

where  $\varepsilon > 0$

$$M_F = \max_{i=1, \dots, n} \left( \sum_{j=1}^n |a_{ij}| + \sum_{k=1}^n |a_{ik}^\tau| V_F \right)$$

$$V_F = \max_{t \in [-\tau, 0]} \|v(t)\|_\infty.$$

Consider sets

$$\Xi \doteq [-v_M, v_M]^n \subset \mathbb{R}^n, \quad \Upsilon \doteq [T_{amb}, T_M]^n \subset \mathbb{R}^n.$$

**Lemma 1:** Let  $(v(t), g(t), T(t))$ ,  $t \geq -\tau$ , be a solution of an IVP associated with (19)–(21). Then, there exists  $t_f < \infty$  such that  $v(t) \in \Xi$  and  $T(t) \in \Upsilon$  for  $t \geq t_f$ . Moreover,  $\Xi \times \Gamma \times \Upsilon \subset \mathbb{R}^{3n}$  is positively invariant for the dynamics of (19)–(21).

*Proof:* See Appendix A. ■

Due to Lemma 1, there is no loss in generality if we study CS of SMNNs obeying the following delayed DVI

$$\dot{v}(t) \in -\mathfrak{D}(v(t), g(t)) + A \mathfrak{F}(v(t)) + A^\tau \mathfrak{F}(v(t - \tau)) - N_\Xi(v(t)) \quad (24)$$

$$\dot{g}(t) \in \mathfrak{G}(v(t), T(t)) - N_\Gamma(g(t)) \quad (25)$$

$$\dot{T}(t) \in \mathfrak{T}(v(t), g(t)) - \frac{T(t) - \tilde{T}_{amb}}{\Theta_{th}} - N_\Upsilon(T(t)) \quad (26)$$

where the ICs  $v \in C^0([-\tau, 0], \Xi)$ ,  $g_0 \in \Gamma$  and  $T_0 \in \Upsilon$ .

**Proposition 1:** There exists a unique solution of any IVP associated with a SMNN which is defined and bounded for  $t \geq -\tau$ .

*Proof:* See Appendix B. ■

Note that, although a SMNN obeys a DVI defined by a discontinuous and multivalued vector field, yet it enjoys the important property of uniqueness of the solution of any IVP.

### III. CS OF SMNNs

In Section III-A and III-B we establish the main results on CS of a SMNN. Then, in Section III-C we provide a number of remarks to discuss the significance of the obtained results.

#### A. Main Result

Next, we recall the classical definition of CS.

**Definition 3 ([23], [24], [25], [26]):** A SMNN is said to be completely stable if and only if the solution of any IVP converges to an EP as  $t \rightarrow \infty$ .

To study CS we enforce the following assumption on the interconnection and delayed interconnection matrices  $A$  and  $A^\tau$ , and the diagonal matrix  $D_m$  in (18).

**Assumption 2:** There exist a diagonal matrix  $P \in \mathbb{R}^{n \times n}$ ,  $P > 0$ , and a matrix  $Z = Z^\top \in \mathbb{R}^{n \times n}$ ,  $Z > 0$ , such that the following condition

$$S = \begin{pmatrix} 2PD_m - PA - A^\top P - Z & -PA^\tau \\ -A^\tau P & Z \end{pmatrix} > 0 \quad (27)$$

holds.

It can be readily verified that condition (27) amounts to solve an LMI feasibility problem with respect to the  $n$  positive

diagonal entries of the matrix  $P$  and the  $n(n+1)/2$  entries of the positive definite matrix  $Z$ . It is well known that there are effective numerical tools based on interior point polynomial methods for solving such LMI problems [31], [56]. Moreover, the particular structure of the matrix  $S$  makes it possible to derive some necessary and simplified sufficient conditions for (27), as discussed in Section III-B.

The next fundamental result holds.

*Theorem 1:* Suppose that Assumptions 1 and 2 are satisfied. Let

$$k \doteq \min \left\{ \frac{\Lambda_m(S)\gamma^2(v_M)}{4\Lambda_M(P)}, \frac{1}{2\tau} \ln \left( 1 + \frac{\Lambda_m(S)}{2\Lambda_M(PA^\tau Z^{-1}(PA^\tau)^\top)} \right) \right\} > 0 \quad (28)$$

where  $v_M$  is given in (22) and  $\gamma(v_M)$  is defined in Assumption 1. Then, the solution of any IVP associated with the SMNN (24)–(26) converges exponentially to an EP  $(\bar{v}, \bar{g}, \bar{T})$  depending upon the ICs and such that  $\bar{v} = 0$ ,  $\bar{g} \in \Gamma$  and  $\bar{T} = \bar{T}_{\text{amb}}$ . More precisely, the convergence rate can be estimated as follows. There exist  $M_v, M_g, M_T > 0$  such that

$$\begin{aligned} \|v(t)\| &\leq M_v e^{-kt} \\ \|g(t) - \bar{g}\| &\leq M_g e^{-kt} \\ \|T(t) - \bar{T}_{\text{amb}}\| &\leq M_T e^{-k_T t} \end{aligned}$$

for  $t \geq 0$ , where  $k_T = \min\{k, 1/\Theta_{\text{th}}\}$ .

*Proof:* See Appendix C. ■

*Theorem 2:* A SMNN is completely stable if Assumptions 1 and 2 are satisfied.

*Proof:* Immediately follows from Theorem 1. ■

## B. Checking Conditions for CS

Since Assumption 2 plays a key role to assess CS via Theorem 2, in this section we investigate if it is possible to explicitly characterize classes of matrices  $A$ ,  $A^\tau$ , and  $D_m$  for which condition (27) holds. We first show that (27) admits an equivalent condition.

*Lemma 2:* The diagonal matrix  $P \in \mathbb{R}^{n \times n}$ ,  $P > 0$ , and the matrix  $Z = Z^\top \in \mathbb{R}^{n \times n}$ ,  $Z > 0$ , solve (27) if and only if the matrix

$$\Omega \doteq 2PD_m - PA - A^\top P - Z - PA^\tau Z^{-1}(PA^\tau)^\top \quad (29)$$

is positive definite.

*Proof:* It follows directly by applying Schur's lemma [51] to the matrix  $S$ . ■

Next, we investigate how both necessary and sufficient conditions ensuring that  $\Omega > 0$  can be derived using the class of LDS matrices (see Section I-A3). Assessing whether a given matrix is LDS in general amounts to solving a simplified LMI feasibility problem. Moreover, as shown later, there are relevant classes of matrices for which checking the LDS condition can be performed analytically via a finite number of inequalities.

*1) Necessary Conditions:* First of all, we observe that  $\Omega > 0$  if and only if

$$2PD_m - PA - A^\top P > Z + PA^\tau Z^{-1}(PA^\tau)^\top \quad (30)$$

where the right-side term is positive definite for all  $Z > 0$ . Hence, inequality (30) can be solved for  $P$  and  $Z$  only if  $P(D_m - A) + (D_m - A^\top)P > 0$ , which implies that a necessary condition for Assumption 2 is that  $D_m - A$  is LDS.

Indeed, a sharpened necessary condition is derived next.

*Proposition 2:* Assumption 2 holds only if both matrices  $D_m - A - A^\tau$  and  $D_m - A + A^\tau$  are LDS.

*Proof:* We first observe that the condition

$$(PA^\tau \pm Z)Z^{-1}(PA^\tau \pm Z)^\top \geq 0$$

is satisfied for all  $P$  and  $Z > 0$ . Hence, we have

$$Z + PA^\tau Z^{-1}(PA^\tau)^\top \geq \mp(PA^\tau + (PA^\tau)^\top)$$

which implies that  $\Omega > 0$  only if

$$2PD_m - PA - A^\top P \pm (PA^\tau + (PA^\tau)^\top) > 0$$

thus completing the proof. ■

*2) Sufficient Conditions:* A sufficient condition ensuring that  $\Omega > 0$  can be derived by finding a diagonal matrix  $P$ ,  $P > 0$ , such that  $D_m - A$  is LDS and condition (30) is satisfied for some choice of the matrix  $Z$ ,  $Z > 0$ . Suppose that  $A^\tau$  is nonsingular and assume that  $Z$  enjoys the following structure

$$Z = Z_0 \doteq \alpha(A^\tau)^\top PA^\tau \quad (31)$$

where  $\alpha$  is a positive real parameter. Clearly,  $Z_0 > 0$  for all  $\alpha > 0$ . By replacing  $Z$  with  $Z_0$  in (30) we have

$$2PD_m - PA - A^\top P > \alpha(A^\tau)^\top PA^\tau + \frac{1}{\alpha}P. \quad (32)$$

Clearly, if  $P$  is a solution of (32) for a given  $\alpha > 0$ , then  $\Omega > 0$  once  $Z = Z_0$ . We observe that for a given  $P > 0$  condition (32) holds for some  $\alpha > 0$  only if it is satisfied for the value of  $\alpha$  which makes its right-side as less positive definite as possible. This can be pursued by noting that the following condition

$$\alpha \|A^\tau\|_2^2 \Lambda_M(P) + \frac{1}{\alpha} \Lambda_M(P) \geq \alpha(A^\tau)^\top PA^\tau + \frac{1}{\alpha}P \quad (33)$$

holds for all  $\alpha > 0$ . Hence, a suitable choice for minimizing the right-side term of (32) consists in selecting for  $\alpha$  the value which minimizes the left-side of (33), which amounts to

$$\alpha = \frac{1}{\|A^\tau\|_2}. \quad (34)$$

This leads to the next result.

*Proposition 3:* Let  $A^\tau$  be nonsingular. Then, Assumption 2 holds if there exists a diagonal matrix  $P$ ,  $P > 0$ , such that

$$2PD_m - PA - A^\top P - \frac{1}{\|A^\tau\|_2} (A^\tau)^\top PA^\tau - \|A^\tau\|_2 P > 0. \quad (35)$$

*Proof:* It is enough to replace  $\alpha$  in (32) with the expression in (34). ■

The above sufficient condition requires the solution of an LMI feasibility problem involving the  $n$  positive entries of  $P$

and the assumption that  $A^\tau$  is nonsingular. Indeed, if  $A^\tau$  is singular then a similar reasoning can be applied by adding to  $Z_0$  an arbitrarily small diagonal positive definite matrix. The analysis requires some technical developments and it is omitted for space limitations.

Now, we derive a sufficient condition which is more simple to be checked and it covers also the case when  $A^\tau$  is singular. Indeed, we exploit the fact that if  $P > 0$  is such that  $D_m - A$  is LDS then there exists  $\delta > 0$  ensuring that

$$2PD_m - PA - A^\top P \geq \delta \Lambda_M(P) I_n \quad (36)$$

where  $I_n$  is the  $n$ th-order identity matrix.

*Proposition 4:* Let  $P > 0$  satisfy condition (36) for some  $\delta > 0$ . Then, Assumption 2 holds if

$$\|A^\tau\|_2 < \frac{\delta}{2}. \quad (37)$$

*Proof:* Let

$$Z = \Lambda_M(P) \|A^\tau\|_2 I_n$$

where  $P, P > 0$ , solves (36) for some  $\delta$ . According to this choice of  $Z$ , (30) boils down to

$$2PD_m - PA - A^\top P > \Lambda_M(P) \|A^\tau\|_2 I_n + \frac{1}{\Lambda_M(P) \|A^\tau\|_2} PA^\tau (PA^\tau)^\top.$$

To prove that the above condition is satisfied, it is enough to observe that (37) ensures that  $P$  is such that

$$2PD_m - PA - A^\top P > 2\Lambda_M(P) \|A^\tau\|_2 I_n$$

and to verify that the following condition

$$\Lambda_M(P) \|A^\tau\|_2 I_n \geq \frac{1}{\Lambda_M(P) \|A^\tau\|_2} PA^\tau (PA^\tau)^\top$$

holds true.  $\blacksquare$

Clearly, the larger is  $\delta$  the larger is the set of matrices  $A^\tau$  to which Proposition 4 applies. In the next theorem, which summarizes and at the same time improves the previous sufficient conditions, we address the problem of choosing the optimal value of  $\delta$ .

*Theorem 3:* Suppose that Assumption 1 is satisfied and  $D_m - A$  is LDS. Let the diagonal matrix  $P^*$ ,  $P^* > 0$ , be such that  $(D_m - A^\top)P^* + P^*(D_m - A) > 0$  and let

$$d^* = \frac{1}{2} \Lambda_m \left[ ((D_m - A^\top)P^* + P^*(D_m - A)P^*) (P^*)^{-1} \right] > 0. \quad (38)$$

Then, a SMNN is CS for all the matrices  $A^\tau$  satisfying

$$\|A^\tau\|_2 < d^* \frac{\Lambda_m(P^*)}{\Lambda_M(P^*)}. \quad (39)$$

*Proof:* Consider

$$2P^*D_m - P^*A - A^\top P^* \geq 2dP^* \quad (40)$$

where  $d$  is a positive scalar. It is seen that  $d^*$  in (38) is the maximum value of  $d$  such that (40) holds. Now, exploiting the relation  $[\Lambda_m(P^*)]^{-1}P^* \geq I_n$  we have

$$2P^*D_m - P^*A - A^\top P^* \geq 2d^* \Lambda_m(P^*) I_n.$$

Hence, by comparing the above condition with (36) it can be concluded that, once  $d^*$  and  $P^*$  are available, condition (37) boils down to (39). This ensures that Assumption 2 is satisfied and a SMNN is CS due to Theorem 2.  $\blacksquare$

We stress that Theorem 3 provides a robustness condition for CS with respect to the magnitude of the induced matrix 2-norm of  $A^\tau$ . The theorem assumes that  $D_m - A$  is LDS, whose verification in general requires to solve an LMI problem with  $n$  unknowns. However, there are relevant classes of matrices for which  $D_m - A$  is LDS is equivalent to  $D_m - A \in \mathcal{P}$  and hence it can be simply checked via a finite number of inequalities (see Section I-A3).

*Theorem 4:* Suppose that Assumption 1 is satisfied. Then, the result on CS in Theorem 3 holds if (39) is met and one of the following conditions is satisfied:

- 1)  $D_m - A$  is symmetric and positive definite, i.e.,  $D_m - A \in \mathcal{P}$ ;
- 2)  $D_m - A$  is an  $M$ -matrix;
- 3)  $D_m - A$  is a nonsingular  $H$ -matrix with nonnegative diagonal entries;
- 4)  $D_m - A$  is an acyclic  $\mathcal{P}$  matrix; and
- 5)  $D_m - A$  is a skew symmetric matrix with positive diagonal entries.

*Proof:* All the stated conditions ensure that  $D_m - A$  is LDS [52].  $\blacksquare$

### C. Discussion

- 1) To the authors knowledge, Theorems 2–4 are the first results on CS of MNNs with a class of extended memristors. The memristors obey Stanford model and they can accurately describe the behavior of real memristor devices. These theorems are not only of theoretic interest, but they can also be used to design practical MNNs in nanotechnology enjoying the important property of CS.
- 2) Previous papers [48] and [49] deal with CS of MNNs with the TEAM memristor model and a memristor model used in ameba learning. Such models are not as general and effective as the Stanford model to describe the dynamics of real memristor devices [16]. Moreover, [49] deals with CS of a class of NNs without delay and with uncoupled second-order cells.
- 3) According to Theorems 1–4, a SMNN is not only completely stable but in addition each solution is exponentially convergent to an EP with a known convergence rate not depending on the ICs. This enables to quantitatively estimate the convergence time, which is extremely useful in view of the practical applications to the solution in real time of signal processing problems. It is also noticed that exponential convergence holds even if a SMNN has a continuum of EPs (see Section II-C).
- 4) We stress that the approach based on LDS matrices in Theorems 3 and 4 enables to single out entire classes of matrices and ranges of interconnections parameters for which a SMNN is CS. Furthermore, such results hold for a general SMNN dimension  $n$ . We will further illustrate these features in Section IV. Theorem 4 can

be immediately extended to any other class of matrices enjoying the LDS property [52].

- 5) Theorems 1–4 ensure that exponential stability and CS of SMNNs enjoy fundamental robustness properties. First of all, exponential stability and CS are insensitive to the presence of arbitrary constant delays. This is of potential interest for the applications, because delays are subject to large uncertainties due to the difficulties to accurately measure them [57]. Moreover, the condition  $D_m - A$  is LDS is robust with respect to small variations of  $A$ . Finally, if  $D_m - A$  is LDS, Theorem 3 shows that there is a robustness margin with respect to the norm of the delayed interconnection matrix  $A^\tau$ .
- 6) Due to Theorem 1, we have that  $T_i(t) \rightarrow T_{\text{amb}}$  and  $v_i(t) \rightarrow 0$ ,  $i = 1, \dots, n$ , as  $t \rightarrow \infty$ . This means that, when a steady state is reached, the memristor is at the ambient temperature, moreover, voltages, currents and power in the SMNN vanish. This is a remarkable advantage over traditional NNs as cellular NNs or Hopfield NNs where the asymptotic values of voltages are not zero and the NNs consume power also at a steady state. It is worth to stress that, although voltages vanish, yet the nonvolatile memristors can retain in memory the result of the computation, i.e., the asymptotic values  $\bar{g}_i$ ,  $i = 1, \dots, n$ , of the gaps, according to the principle of in-memory computing. In practice, the values  $\bar{g}_i$  can be measured by a standard technique for evaluating the small signal memductances  $(I_0/V_0)e^{-\bar{g}_i/\bar{g}}$  displayed by the memristors [58].
- 7) CS of a NN, i.e., convergence of each trajectory toward an EP, is a peculiar dynamical property that can be guaranteed under suitable hypotheses on the activations and neuron interconnections (see Assumptions 1 and 2 in Theorem 1). On the other hand, in the general case, NNs do not enjoy CS since they can display sustained oscillations, traveling waves and even chaos and hyperchaos, see, e.g., [29], [59], [60], [61], [62], [63], [64], [65], [66], and references therein.

#### IV. NUMERICAL SIMULATIONS

As an application example of the bound (39) for CS of SMNNs, we focus on the case where  $D_m - A$  belongs to the following class of matrices with a cyclic structure

$$C_{r,s} = \begin{pmatrix} r & 0 & \cdots & 0 & s \\ -s & r & \ddots & & 0 \\ 0 & -s & r & \ddots & \vdots \\ \vdots & \ddots & \ddots & \ddots & \vdots \\ 0 & \cdots & 0 & -s & r \end{pmatrix} \in \mathbb{R}^{n \times n} \quad (41)$$

where  $r > 0$ ,  $s > 0$ . Matrices in this class play an important role in modeling cyclic dynamical systems, food chains, chemical reactions, and also to investigate nonlinear dynamic phenomena and the potentials and limitations for information processing of cellular NN arrays [67], [68].

Next, we report known conditions ensuring that these matrices are LDS (see [69]).

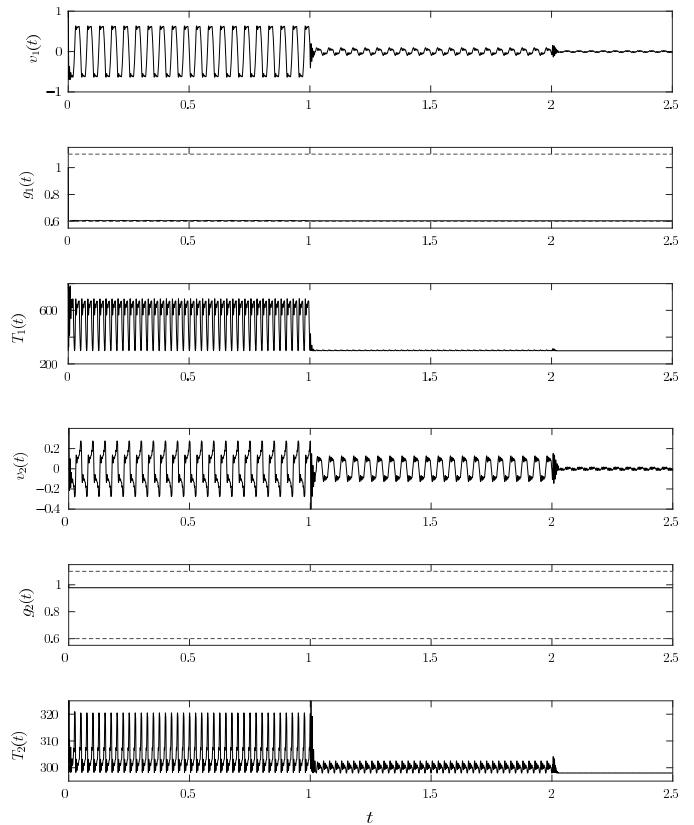


Fig. 1. Time evolution of memristor voltages  $v_i$  [V], gaps  $g_i$  [nm] and temperatures  $T_i$  [K],  $i = 1, 2$  for a 5-neuron SMNN with a cyclic interconnecting structure. Time  $t$  in  $\mu\text{s}$ .

*Lemma 3:* Matrix  $C_{r,s}$  is LDS if and only if

$$r - s \cos\left(\frac{\pi}{n}\right) > 0. \quad (42)$$

Exploiting this condition, we obtain the following result as a consequence of Theorem 3.

*Corollary 1:* Consider a SMMN where  $D_m - A = C_{r,s}$  satisfies (42) and suppose that Assumption 1 holds. Then, the SMNN is CS for all  $A^\tau$  such that

$$\|A^\tau\|_2 < r - s \cos\left(\frac{\pi}{n}\right). \quad (43)$$

*Proof:* The proof follows by computing the right-side of (39) for  $D_m - A = C_{r,s}$  and  $P^* = I_n$ . Indeed, we have  $\Lambda_M(P^*) = \Lambda_m(P^*) = 1$  and

$$d^* = \Lambda_m\left(\frac{1}{2}(C_{r,s}^\top + C_{r,s})\right).$$

Exploiting the eigenvalues-eigenvectors properties of  $C_{r,s}$  we obtain

$$\Lambda_m\left(\frac{1}{2}(C_{r,s}^\top + C_{r,s})\right) = r - s \cos\left(\frac{\pi}{n}\right)$$

which completes the proof.  $\blacksquare$

It is worth noting that (43) provides the tightest bound on  $\|A^\tau\|_2$  for which Assumption 2 holds, when  $D_m - A = C_{r,s}$ . In fact, consider  $A^\tau = \lambda I_n$  with  $\lambda > 0$ . We have

$$D_m - A - A^\tau = C_{r-\lambda,s}$$



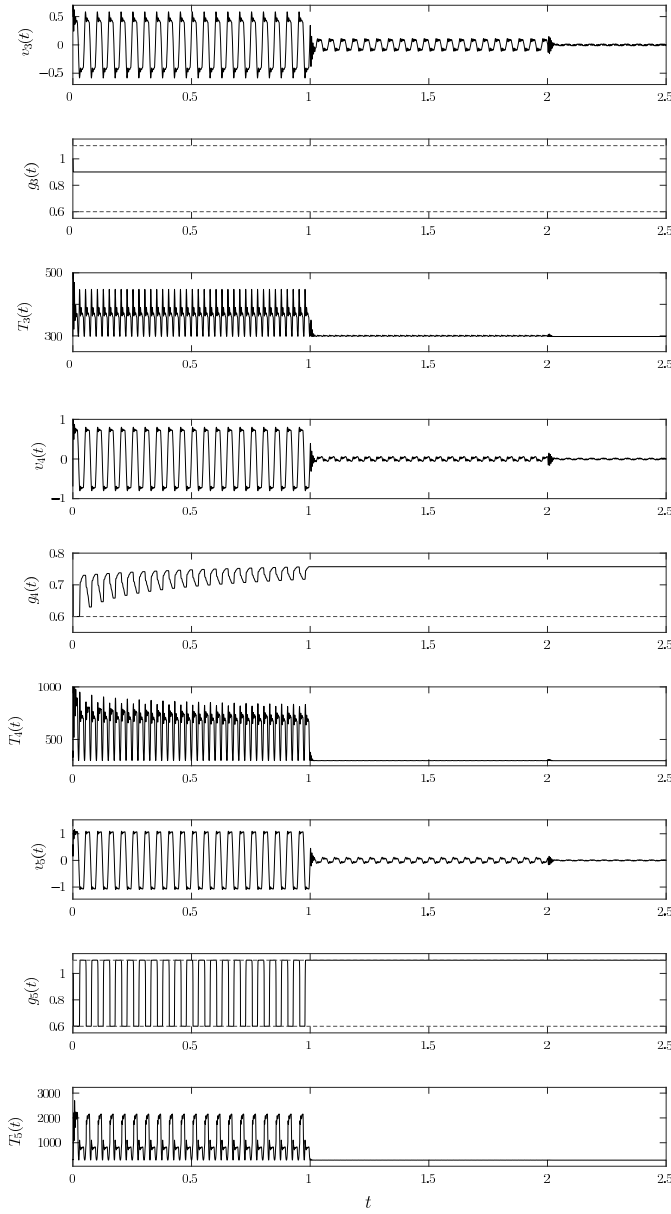


Fig. 2. Time evolution of memristor voltages  $v_i$  [V], gaps  $g_i$  [nm] and temperatures  $T_i$  [K],  $i = 3, 4, 5$ , for a 5-neuron SMNN with a cyclic interconnecting structure. Time  $t$  in  $\mu\text{s}$ .

and hence, according to Lemma 3, the necessary condition of Proposition 2 is satisfied if and only if

$$\lambda < r - s \cos\left(\frac{\pi}{n}\right).$$

Since  $\|A^\tau\|_2 = \lambda$ , Assumption 2 holds if and only if (43) is satisfied.

For illustration purposes, choose  $A^\tau$  with the following structure:

$$A^\tau = \begin{pmatrix} u & 0 & \cdots & 0 & u \\ u & u & \ddots & & 0 \\ 0 & u & u & \ddots & \vdots \\ \vdots & \ddots & \ddots & \ddots & \vdots \\ 0 & \cdots & 0 & u & u \end{pmatrix} \in \mathbb{R}^{n \times n}. \quad (44)$$

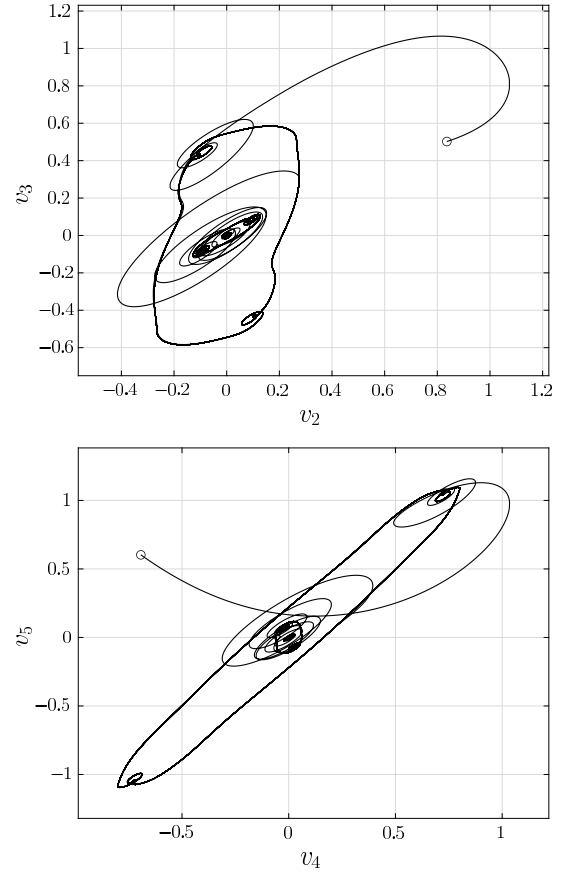


Fig. 3. Voltage  $v_3$  as a function of  $v_2$  (upper plot) and  $v_5$  as a function of  $v_4$  (lower plot) for a 5-neuron SMNN. The IC is marked with a circle. Voltages in V.

It can be easily checked that  $\|A^\tau\|_2 = 2|u|$ , hence due to Corollary 1, we have that Assumption 2 is satisfied and the SMNN is CS if (42) holds and in addition

$$|u| < u_M \doteq \frac{r}{2} - \frac{s}{2} \cos\left(\frac{\pi}{n}\right).$$

This demonstrates that it is easy to find in an analytic way, via the developed LDS approach, conditions for CS that hold for an entire class of  $A$  and  $A^\tau$ , for open ranges of interconnection parameters and for any dimension  $n$ .

Let  $n = 5$  neurons,  $\tau = 10^{-6}$  s,  $C = 10^{-9}$  F and consider the neuron activation  $f(\gamma) = (1/2)(|\gamma + 5| - |\gamma - 5|)$ , which satisfies Assumption 1. Also, refer to the memristor parameters in Table I. Note in particular that the gap  $g \in [g_m, g_M] = [0.6, 1.1]$  nm.

The EPs of the SMNN are such that the voltages  $\bar{v}_i = 0$ , the temperatures  $\bar{T}_i = T_{\text{amb}}$ , while the gap  $\bar{g}_i$  can assume any value in  $[g_m, g_M]$ ,  $i = 1, \dots, 5$ . Note that there is a continuum of nonisolated EPs.

Let  $A = -C_{p,q}$  with  $p = 1.83$ ,  $q = 2$ . We have  $D_m = 2.84 \cdot 10^{-4} I_5$ , hence  $D_m - A = C_{r,s}$  with  $r = 1.8303$ ,  $s = 2$  and so  $u_M = 0.2123$ . Next, we fix  $u = 0.1$ , so that  $2u < u_M$ . We have simulated the SMNN using MATLAB routine `dde23` for delayed differential equations. The hard constraints  $g_i \in [g_m, g_M]$  for the neuron gaps are imposed numerically using the “Events” option in MATLAB environment (see [70]). The ICs  $v_i(t) \in C([-10^{-6}, 0], \mathbb{R})$  for the neuron voltages are

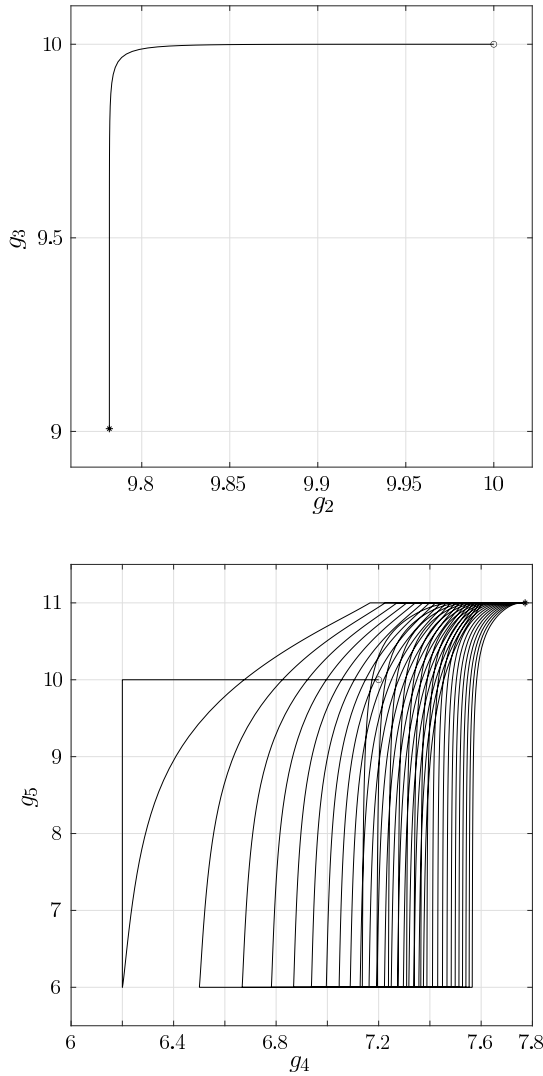


Fig. 4. Gap  $g_3$  as a function of  $g_2$  (upper plot) and  $g_5$  as a function of  $g_4$  (lower plot) for a 5-neuron SMNN. The IC is marked with a circle and the final state with an asterisk. Gaps in nm.

sinusoidal functions with angular frequency  $20/\tau$  and with various phases and amplitudes (details are omitted). The ICs of the gaps are  $g_1(0) = g_2(0) = g_3(0) = g_5(0) = 1$  nm, while  $g_4(0) = 0.7$  nm. The ICs of the temperatures are  $T_i(0) = T_{\text{amb}} = 298$  K for  $i = 1, \dots, 5$ . Figs. 1 and 2 depict the time-domain behavior of memristor voltages  $v_i$ , gaps  $g_i$  and temperatures  $T_i$ ,  $i = 1, \dots, 5$ , in the time interval  $[0, 2.5 \times 10^{-6}] = [0, 2.5\tau]$ . Fig. 3 reports  $v_3$  as a function of  $v_2$  and  $v_5$  as a function of  $v_4$ , while Fig. 4 shows  $g_3$  as a function of  $g_2$  and  $g_5$  as a function of  $g_4$ . It is seen that after some transient oscillations  $v_i$ ,  $g_i$  and  $T_i$  tend to constant values. In particular, voltages tend to 0, while temperatures tend to  $T_{\text{amb}}$ . The gaps tend to constant values  $\bar{g}_1 = 0.605$ ,  $\bar{g}_2 = 0.978$ ,  $\bar{g}_3 = 0.901$ ,  $\bar{g}_4 = 0.757$  and  $\bar{g}_5 = 1.1$  nm. The asymptotic values  $\bar{g}_i$ , which are retained in memory by the nonvolatile memristors, are the result of the SMNN computation. The observed behavior is in accordance with that predicted by Corollary 1.

## V. CONCLUSION

The article has proved some fundamental results on CS for a class of delayed NNs with nonvolatile extended memristors obeying the popular and widely used Stanford model. The conditions for CS, which can be effectively checked numerically via an LMI approach, or analytically, using the concept of LDS matrices, hold for any value of the concentrated delay and they are robust with respect to variations of the interconnections. Using the LDS approach, several classes of interconnection matrices have been singled out for which CS holds. The obtained results not only ensure CS but also exponential convergence of each trajectory with a known convergence rate which is independent of the ICs. Furthermore, at the end of the transient power in the SMNN vanishes, while nonvolatile memristors retain in memory the result of computation in accordance with the in-memory computing principle. Future work will be devoted to study whether the techniques developed in the article can be applied or extended to study CS of NNs with other memristors used for modeling real devices in nanotechnology.

## APPENDIX A PROOF OF LEMMA 1

Consider an IVP for (19)–(21) and suppose without loss of generality  $C = 1$ . We first show that there exists  $t'_f < \infty$  such that  $v(t) \in \Xi$  for  $t \geq t'_f$ . Consider  $v(0) \in \mathbb{R}^n$ ,  $\|v(0)\|_\infty > v_M$ . Let  $\hat{i}(t) \in \{1, \dots, n\}$  be such that  $|v_{\hat{i}(t)}| = \|v(t)\|_\infty$ . Let us first consider  $t \in [0, \tau]$ . Function  $\|v(t)\|_\infty$  is absolutely continuous on  $[0, \tau]$ , hence it is differentiable for a.a.  $t \in [0, \tau]$ . By proceeding as in [38, Appendix A], we have for a.a.  $t \in [0, \tau]$

$$\begin{aligned} \frac{d\|v(t)\|_\infty}{dt} &= \text{sgn}(v_{\hat{i}(t)}(t)) \left( -I_0 e^{-\frac{g_{\hat{i}(t)}}{\bar{g}_{\hat{i}(t)}}} \sinh\left(\frac{v_{\hat{i}(t)}(t)}{V_0}\right) \right. \\ &\quad + \sum_{j=1}^n a_{i(t)j} f(v_j(t)) \\ &\quad \left. + \sum_{k=1}^n a_{i(t)k}^\tau f(v_k(t-\tau)) \right) \end{aligned}$$

where  $\text{sgn}$  denotes the signum function. As long as  $\|v(t)\|_\infty > v_M$ , we have  $|v_{\hat{i}(t)}(t)| > v_M \geq 1$ . Considering that  $|f(v_j(t))| \leq |v_j(t)|$  and  $|v_j(t-\tau)| < V_F$  for any  $j \in \{1, \dots, n\}$  and  $t \in [0, \tau]$ , we have

$$\begin{aligned} \frac{d\|v(t)\|_\infty}{dt} &\leq |v_{\hat{i}(t)}(t)| \left( -\frac{I_0}{V_0} e^{-\frac{g_M}{\bar{g}_M}} \left( 1 + \frac{v_M^2}{6V_0^2} \right) \right. \\ &\quad \left. + \sum_{j=1}^n |a_{i(t)j}| + \sum_{k=1}^n |a_{i(t)k}^\tau| V_F \right). \end{aligned}$$

Taking into account (22) the inequality boils down to

$$\frac{d\|v(t)\|_\infty}{dt} \leq -|v_{\hat{i}(t)}| \varepsilon \leq -\varepsilon. \quad (45)$$

As a consequence, in  $[0, \tau]$ ,  $\|v(t)\|_\infty$  decreases toward  $v_M$  with velocity not smaller than  $\varepsilon$ . Moreover, since  $\|v(t)\|_\infty$  is monotone decreasing,  $\max_{t \in [0, \tau]} |v_{\hat{i}(t)}| \leq \max_{t \in [-\tau, 0]} \|v(t)\|_\infty = V_F$  and inequality (45) holds

true for any  $t \in [k\tau, (k+1)\tau]$ ,  $k = 1, 2, \dots$ , as long as  $\|v(t)\|_\infty \geq v_M$ . This implies that  $\Xi$  is globally attracting and positively invariant. Moreover,  $v(t) \in \Xi$  for  $t \geq t'_f = (\|v_i(0)\|_\infty - v_M)/\varepsilon$ .

Now, assume that  $T(t'_f) \notin \Upsilon$  and let  $\eta_T \neq \emptyset$  be the set of indexes  $i \in \{1, \dots, n\}$  such that  $T_i(t'_f) > T_M$ . It can be checked that the following inequality holds if  $T_i(t) \geq T_M$

$$\dot{T}_i(t) \leq -\frac{T_M - T_{\text{amb}}}{\Theta_{\text{th}}} + I_0 e^{-\frac{gm}{\varepsilon}} \sinh\left(\frac{v_M}{V_0}\right) v_M \leq -\varepsilon.$$

Arguing as before we conclude that  $\Upsilon$  is globally attracting and positively invariant. Moreover,  $T(t) \in \Upsilon$  for  $t \geq t_f = t'_f + t''_f$  where  $t''_f = \max_{i \in \eta_T} (|T_i(t'_f)| - T_M)/\varepsilon$ .

From the proof it also follows that  $\Xi \times \Gamma \times \Upsilon$  is positively invariant for the dynamics of (19)–(21). ■

## APPENDIX B PROOF OF PROPOSITION 1

We proceed by using the method of steps. First, we show the existence of a solution for an IVP associated with (24)–(26) for  $t \in [-\tau, \tau]$ . Consider the following IVP for a DVI without delay

$$\begin{aligned} \begin{pmatrix} \dot{w} \\ \dot{h} \\ \dot{u} \\ \dot{\theta} \end{pmatrix} &\in \begin{pmatrix} -\mathfrak{D}(w, h) + A\mathfrak{F}(w) + A^\tau \mathfrak{F}(v(\theta)) \\ \mathfrak{G}(w, u) \\ \mathfrak{T}(w, h) - \frac{u - \tilde{T}_{\text{amb}}}{\Theta_{\text{th}}} \\ 1 \end{pmatrix} \\ &- \begin{pmatrix} N_\Xi(w) \\ N_\Gamma(h) \\ N_\Upsilon(u) \\ N_{[-\tau, 0]}(\theta) \end{pmatrix} \\ &= F(w, h, u, \theta) - N_{\Xi \times \Gamma \times \Upsilon \times [-\tau, 0]}(w, h, u, \theta) \end{aligned} \quad (46)$$

where  $t \in [0, \tau]$  and the ICs are  $w(0) = v(0)$ ,  $h(0) = g_0$ ,  $u(0) = T_0$  and  $\theta(0) = -\tau$ . Since  $\Xi \times \Gamma \times \Upsilon \times [-\tau, 0] \subset \mathbb{R}^{3n+1}$  is a nonempty compact convex set,  $F : \Xi \times \Gamma \times \Upsilon \times [-\tau, 0] \rightarrow \mathbb{R}^{3n+1}$  is continuous in  $\Xi \times \Gamma \times \Upsilon \times [-\tau, 0]$  and the ICs belong to  $\Xi \times \Gamma \times \Upsilon \times [-\tau, 0]$ , due to Property 1 there exists at least one solution to the IVP (46). The solution is bounded for  $t \geq -\tau$  due to Lemma 1.

Solving the IVP for  $\theta$ , we obtain  $\theta(t) = t - \tau$  in  $t \in [0, \tau]$ . Substituting in (46), we can check that

$$(v, g, T) = \begin{cases} (v, g_0, T_0), & t \in [-\tau, 0] \\ (w, h, u), & t \in [0, \tau] \end{cases} \quad (47)$$

is a solution of (24)–(26) for  $t \in [-\tau, \tau]$ .

Now, it is possible to proceed in a way analogous to that used to prove Property 4 in [71] to show that (24)–(26) admits at least one solution in the interval  $[-\tau, (m+1)\tau]$  for any integer  $m > 1$ , thus completing the proof of the existence part. The solution is bounded due to Lemma 1.

To prove the uniqueness of the solution, suppose for contradiction that there exist two solutions  $(v_a, g_a, T_a)$  and  $(v_b, g_b, T_b)$  of an IVP for (24)–(26) and define their distance

$$\Delta = \frac{1}{2} (\|v_a - v_b\|^2 + \|g_a - g_b\|^2 + \|T_a - T_b\|^2). \quad (48)$$

We want to prove that  $\Delta(t) = 0$  for any  $t \in [-\tau, m\tau]$ , where  $m \geq 0$  is an integer. This result holds for  $m = 0$ , since for  $t \in [-\tau, 0]$  both solutions coincide with  $(v, g_0, T_0)$ , therefore  $\Delta(t) = 0$ . To prove the uniqueness for  $m > 0$ , we apply again the method of steps. Suppose that  $\Delta(t) = 0$  for  $t \in [-\tau, m\tau]$ . For  $t \in [-m\tau, (m+1)\tau]$  we have

$$\begin{cases} \dot{v}_a(t) = -\mathfrak{D}(v_a(t), g_a(t)) + A\mathfrak{F}(v_a(t)) \\ \quad + A^\tau \mathfrak{F}(v_a(t - \tau)) - n_{v_a, t} \\ \dot{g}_a(t) = \mathfrak{G}(v_a(t), T_a(t)) - n_{g_a, t} \\ \dot{T}_a(t) = \mathfrak{T}(v_a(t), g_a(t)) - \frac{T_a(t) - \tilde{T}_{\text{amb}}}{\Theta_{\text{th}}} - n_{T_a, t} \\ \dot{v}_b(t) = -\mathfrak{D}(v_b(t), g_b(t)) + A\mathfrak{F}(v_b(t)) \\ \quad + A^\tau \mathfrak{F}(v_b(t - \tau)) - n_{v_b, t} \\ \dot{g}_b(t) = \mathfrak{G}(v_b(t), T_b(t)) - n_{g_b, t} \\ \dot{T}_b(t) = \mathfrak{T}(v_b(t), g_b(t)) - \frac{T_b(t) - \tilde{T}_{\text{amb}}}{\Theta_{\text{th}}} - n_{T_b, t} \end{cases} \quad (49)$$

where  $n_{v_a, t} \in N_\Xi(v_a(t))$ ,  $n_{v_b, t} \in N_\Xi(v_b(t))$ ,  $n_{g_a, t} \in N_\Gamma(g_a(t))$ ,  $n_{g_b, t} \in N_\Gamma(g_b(t))$ ,  $n_{T_a, t}, n_{T_b, t} \in N_\Upsilon(T_a(t))$ .

We obtain

$$\begin{aligned} \dot{\Delta}(t) &= \langle v_a(t) - v_b(t), \dot{v}_a(t) - \dot{v}_b(t) \rangle \\ &\quad + \langle g_a(t) - g_b(t), \dot{g}_a(t) - \dot{g}_b(t) \rangle \\ &\quad + \langle T_a(t) - T_b(t), \dot{T}_a(t) - \dot{T}_b(t) \rangle \end{aligned} \quad (50)$$

where

$$\begin{aligned} \langle v_a(t) - v_b(t), \dot{v}_a(t) - \dot{v}_b(t) \rangle &= -\langle v_a(t) - v_b(t), \mathfrak{D}(v_a(t), g_a(t)) - \mathfrak{D}(v_b(t), g_b(t)) \rangle \\ &\quad + \langle v_a(t) - v_b(t), A\mathfrak{F}(v_a(t)) - A\mathfrak{F}(v_b(t)) \rangle \\ &\quad + \langle v_a(t) - v_b(t), A^\tau \mathfrak{F}(v_a(t - \tau)) - A^\tau \mathfrak{F}(v_b(t - \tau)) \rangle \\ &\quad - \langle v_a(t) - v_b(t), n_{v_a, t} - n_{v_b, t} \rangle \end{aligned}$$

moreover

$$\begin{aligned} \langle g_a(t) - g_b(t), \dot{g}_a(t) - \dot{g}_b(t) \rangle &= \langle g_a(t) - g_b(t), \mathfrak{G}(v_a(t), T_a(t)) - \mathfrak{G}(v_b(t), T_b(t)) \rangle \\ &\quad - \langle g_a(t) - g_b(t), n_{g_a, t} - n_{g_b, t} \rangle \end{aligned}$$

and, finally

$$\begin{aligned} \langle T_a(t) - T_b(t), \dot{T}_a(t) - \dot{T}_b(t) \rangle &= \langle T_a(t) - T_b(t), \mathfrak{T}(v_a(t), g_a(t)) - \mathfrak{T}(v_b(t), g_b(t)) \rangle \\ &\quad - \left\langle T_a(t) - T_b(t), \frac{T_a(t) - T_b(t)}{\Theta_{\text{th}}} \right\rangle \\ &\quad - \langle T_a(t) - T_b(t), n_{T_a, t} - n_{T_b, t} \rangle. \end{aligned}$$

The normal cone to a set  $Q$  is a monotone operator (see Section I-A1). Moreover, by assumption,  $\Delta(t) = 0$ , i.e.,  $v_a(t - \tau) = v_b(t - \tau)$ , for  $t \in [-\tau, m\tau]$ . Then, from (50)

$$\begin{aligned} \dot{\Delta} &\leq \left\langle \begin{pmatrix} v_a - v_b \\ g_a - g_b \\ T_a - T_b \end{pmatrix}, \right. \\ &\quad \left. \begin{pmatrix} -\mathfrak{D}(v_a, g_a) + A\mathfrak{F}(v_a) + \mathfrak{D}(v_b, g_b) - A\mathfrak{F}(v_b) \\ \mathfrak{G}(v_a, T_a) - \mathfrak{G}(v_b, T_b) \\ \mathfrak{T}(v_a, g_a) - \mathfrak{T}(v_b, g_b) \end{pmatrix} \right\rangle. \end{aligned} \quad (51)$$

By assumption,  $\mathfrak{F}(\cdot)$  is Lipschitz continuous. From (15)–(17), also  $\mathfrak{D}(\cdot, \cdot)$ ,  $\mathfrak{G}(\cdot, \cdot)$ ,  $\mathfrak{I}(\cdot, \cdot)$  are Lipschitz continuous in  $\Xi \times \Gamma \times \Upsilon$ . Then, there exists  $0 < \xi < +\infty$  such that

$$\dot{\Delta}(t) \leq 2\xi \Delta(t).$$

Therefore, by the Gronwall lemma, we obtain

$$0 \leq \Delta(t) \leq \Delta(m\tau) \exp(2\xi(t - m\tau)) = 0 \quad (52)$$

i.e.,  $\Delta(t) = 0$ , when  $t \in [-\tau, m\tau]$ . This shows the uniqueness of the solution for any  $t \geq -\tau$ . ■

### APPENDIX C

To prove Theorem 1 we first establish an algebraic property and a fundamental result on exponential convergence of memristor voltages.

*Proposition 5:* If Assumptions 1 and 2 hold, we have

$$S_k \doteq \begin{pmatrix} 2PD_m - PA - A^\top P - Z - \frac{2kP}{\gamma^2(v_M)} & -PA^\tau \\ -(PA^\tau)^\top & e^{-2k\tau} Z \end{pmatrix} > 0$$

where  $k$  is as in (28).

*Proof:* Since  $Z > 0$ , from Schur's lemma [51],  $S_k > 0$  if and only if

$$\begin{aligned} \Omega_k &= 2PD_m - PA - A^\top P - Z - \frac{2kP}{\gamma^2(v_M)} \\ &\quad - e^{2k\tau} PA^\tau Z^{-1} (PA^\tau)^\top \\ &= \Omega - \frac{2kP}{\gamma^2(v_M)} - (e^{2k\tau} - 1) PA^\tau Z^{-1} (PA^\tau)^\top > 0 \end{aligned}$$

where we have taken into account (29). Since  $k > 0$ ,  $\gamma^2(v_M) > 0$ ,  $P > 0$  and  $PA^\tau Z^{-1} (PA^\tau)^\top > 0$ , we obtain

$$\Lambda_m(\Omega_k) \geq \Lambda_m(\Omega) - \frac{2k}{\gamma^2(v_M)} \Lambda_M(P) - (e^{2k\tau} - 1) \Lambda_M(PA^\tau Z^{-1} (PA^\tau)^\top). \quad (53)$$

If we choose  $k$  as in (28), we have  $\Lambda_m(\Omega_k) > 0$ , which in turn implies  $\Omega_k > 0$  and  $S_k > 0$ . ■

*Lemma 4:* Suppose that Assumptions 1 and 2 hold. Then  $v(t) \rightarrow 0$  exponentially as  $t \rightarrow +\infty$  with convergence rate  $k$  as in (28), i.e.,  $\|v(t)\| \leq M_v e^{-kt}$ ,  $t \geq 0$ , where

$$M_v = \sqrt{\frac{\Lambda_M(P) + \Lambda_M(Z) \frac{1 - e^{-2k\tau}}{2k}}{\Lambda_m(P) \gamma(v_M)}} \max_{-\tau \leq \theta \leq 0} \|v(\theta)\|. \quad (54)$$

*Proof:* Suppose for simplicity  $C = 1$ . Consider for (24)–(26) the following candidate Lyapunov function:

$$\begin{aligned} W(v(t), t) &= e^{2kt} \sum_{i=1}^n 2p_i \int_0^{v_i(t)} f(\sigma) d\sigma \\ &\quad + \int_{t-\tau}^t e^{2ks} \mathfrak{F}^\top(v(s)) Z \mathfrak{F}(v(s)) ds. \end{aligned} \quad (55)$$

Its time derivative along the solutions of (24)–(26) is given by

$$\dot{W}(v(t), t) = 2ke^{2kt} \sum_{i=1}^n 2p_i \int_0^{v_i(t)} f(\sigma) d\sigma$$

$$\begin{aligned} &+ e^{2kt} \sum_{i=1}^n 2p_i f(v_i(t)) \dot{v}_i(t) \\ &+ e^{2kt} \mathfrak{F}^\top(v(t)) Z \mathfrak{F}(v(t)) \\ &- e^{2k(t-\tau)} \mathfrak{F}^\top(v(t-\tau)) Z \mathfrak{F}(v(t-\tau)) \\ &= e^{2kt} \left\{ 2k \sum_{i=1}^n 2p_i \int_0^{v_i(t)} f(\sigma) d\sigma \right. \\ &\quad - 2\mathfrak{F}^\top(v(t)) P \mathfrak{D}(v(t), g(t)) \\ &\quad + 2\mathfrak{F}^\top(v(t)) P A \mathfrak{F}(v(t)) \\ &\quad + 2\mathfrak{F}^\top(v(t)) P A^\tau \mathfrak{F}(v(t-\tau)) \\ &\quad + \mathfrak{F}^\top(v(t)) Z \mathfrak{F}(v(t)) \\ &\quad \left. - e^{-2k\tau} \mathfrak{F}^\top(v(t-\tau)) Z \mathfrak{F}(v(t-\tau)) \right\} \quad (56) \end{aligned}$$

for a.a.  $t \geq 0$ . Considering Assumption 1, we have

$$\begin{aligned} \sum_{i=1}^n 2p_i \int_0^{v_i(t)} f(\sigma) d\sigma &\leq \sum_{i=1}^n 2p_i \int_0^{v_i(t)} \sigma d\sigma \\ &= \sum_{i=1}^n p_i v_i^2(t) \leq \mathfrak{F}^\top(v(t)) \frac{P}{\gamma^2(v_M)} \mathfrak{F}(v(t)). \end{aligned} \quad (57)$$

Additionally, it can be verified that

$$\begin{aligned} &-\mathfrak{F}^\top(v(t)) P \mathfrak{D}(v(t), g(t)) \\ &= -\mathfrak{F}^\top(v(t)) P D(g(t)) V_0 \sinh\left(\frac{v(t)}{V_0}\right) \\ &\leq -\mathfrak{F}^\top(v(t)) P D(g(t)) v(t) \end{aligned} \quad (58)$$

where we have let

$$D(g(t)) = \frac{I_0}{V_0} \text{diag}\left(\exp\left(-\frac{g_1(t)}{\bar{g}}\right), \dots, \exp\left(-\frac{g_n(t)}{\bar{g}}\right)\right).$$

As a consequence, we have

$$\begin{aligned} \dot{W}(v(t), t) &\leq e^{2kt} \left\{ \mathfrak{F}^\top(v(t)) \frac{2kP}{\gamma^2(v_M)} \mathfrak{F}(v(t)) \right. \\ &\quad - \mathfrak{F}^\top(v(t)) 2PD(g(t)) v(t) \\ &\quad + 2\mathfrak{F}^\top(v(t)) P A \mathfrak{F}(v(t)) \\ &\quad + 2\mathfrak{F}^\top(v(t)) P A^\tau \mathfrak{F}(v(t-\tau)) \\ &\quad + \mathfrak{F}^\top(v(t)) Z \mathfrak{F}(v(t)) \\ &\quad \left. - e^{-2k\tau} \mathfrak{F}^\top(v(t-\tau)) Z \mathfrak{F}(v(t-\tau)) \right\}. \end{aligned}$$

Let

$$\mathcal{V}(t, \tau) = \begin{pmatrix} \mathfrak{F}(v(t)) \\ \mathfrak{F}(v(t-\tau)) \end{pmatrix} \in \mathbb{R}^{2n}$$

and

$$\begin{aligned} S(g(t)) &= \begin{pmatrix} 2PD(g(t)) - PA - A^\top P - Z - \frac{2kP}{\gamma^2(v_M)} & -PA^\tau \\ -(PA^\tau)^\top & e^{-2k\tau} Z \end{pmatrix}. \end{aligned}$$

We have  $2PD(g(t)) > 0$  and, due to Assumption 1,  $\mathfrak{F}^\top(v(t)) PD(g(t)) (\mathfrak{F}(v(t)) - v(t)) \leq 0$ . Therefore we can rewrite the last inequality as

$$\begin{aligned} \dot{W}(v(t), t) &\leq -e^{2kt} \left\{ \mathcal{V}^\top(t, \tau) S(g(t)) \mathcal{V}(t, \tau) \right. \\ &\quad \left. + 2\mathfrak{F}^\top(v(t)) P D(g(t)) (\mathfrak{F}(v(t)) - v(t)) \right\} \end{aligned}$$



$$\leq -e^{2kt} \mathcal{V}^\top(t, \tau) S(g(t)) \mathcal{V}(t, \tau). \quad (59)$$

Since

$$S(g(t)) = S_k + \begin{pmatrix} 2P(D(g(t)) - D_m) & 0 \\ 0 & 0 \end{pmatrix} \quad (60)$$

and, from (18),  $D(g(t)) - D_m \geq 0$ , we have  $S(g(t)) \geq S_k$ . As a consequence, from Proposition 5,  $S(g(t)) \geq S_k > 0$ . In turn, this implies (see Assumption 1)

$$\begin{aligned} \dot{W}(v(t), t) &\leq -e^{2kt} \Lambda_M \|\mathfrak{F}(v(t))\|^2 \\ &\leq -e^{2kt} \frac{\Lambda_M}{\gamma^2(v_M)} \|(v(t))\|^2 \leq 0. \end{aligned} \quad (61)$$

We are now in a position to prove that  $v(\cdot)$  converges exponentially to 0. First of all, note that (61) implies that  $W(v(t), t) \leq W(v(0), 0)$  for any  $t \geq 0$ . Then, we have

$$\begin{aligned} W(v(0), 0) &= \sum_{i=1}^n 2p_i \int_0^{v_i(0)} f(\sigma) d\sigma \\ &\quad + \int_{-\tau}^0 e^{2ks} \mathfrak{F}^\top(v(s)) Z \mathfrak{F}(v(s)) ds \\ &\leq v^\top(0) P v(0) \\ &\quad + \int_{-\tau}^0 e^{2ks} \mathfrak{F}^\top(v(s)) Z \mathfrak{F}(v(s)) ds \\ &\leq \Lambda_M(P) \|v(0)\|^2 \\ &\quad + \Lambda_M(Z) \frac{1 - e^{-2k\tau}}{2k} \max_{-\tau \leq \theta \leq 0} \|v(\theta)\|^2 \end{aligned} \quad (62)$$

and

$$\begin{aligned} W(v(t), t) &\geq e^{2kt} \sum_{i=1}^n 2p_i \int_0^{v_i(t)} f(\sigma) d\sigma \\ &\geq e^{2kt} \sum_{i=1}^n 2p_i \int_0^{v_i(t)} \gamma(v_M) \sigma d\sigma \\ &\geq e^{2kt} \Lambda_m(P) \gamma(v_M) \|v(t)\|^2. \end{aligned} \quad (63)$$

Inequalities (62) and (63) can be combined into

$$\begin{aligned} e^{2kt} \Lambda_m(P) \gamma(v_M) \|v(t)\|^2 \\ \leq \left( \Lambda_M(P) + \Lambda_M(Z) \frac{1 - e^{-2k\tau}}{2k} \right) \max_{-\tau \leq \theta \leq 0} \|v(\theta)\|^2 \end{aligned}$$

which yields the stated result.  $\blacksquare$

*Proof of Theorem 1.* Recalling Lemma 1 and noting that  $\sinh(x)$  is convex for  $x \geq 0$  we have from (16) and Lemma 4

$$\begin{aligned} |g(v_i(t), T_i(t))| &\leq v_0 \left| \sinh \left( \frac{q_e a_0 \psi}{\ell K T_{\text{amb}}} v_i(t) \right) \right| \\ &\leq \sinh \left( \frac{q_e a_0 \psi}{\ell K T_{\text{amb}}} v_M \right) |v_i(t)| \\ &\leq v_0 \sinh \left( \frac{q_e a_0 \psi}{\ell K T_{\text{amb}}} v_M \right) M_v e^{-kt} \end{aligned}$$

for  $t \geq 0$ , where  $M_v$  is given in (54). By applying Proposition 6 to (25) it can be easily verified that

$$\|g(t) - \bar{g}\| \leq M_g e^{-kt}, \quad t \geq 0$$

where

$$M_g = \sqrt{n} v_0 \sinh \left( \frac{q_e a_0 \psi}{\ell K T_{\text{amb}}} v_M \right) \frac{M_v}{k}.$$

Arguing as before we have from (17)

$$|t(v_i, g_i)| \leq \frac{I_0}{C_{\text{th}}} \exp \left( -\frac{g_m}{\bar{g}} \right) M_v^2 \sinh \left( \frac{v_M}{V_0} \right) e^{-2kt}$$

for  $t \geq 0$ . By applying Proposition 7 to (26) we obtain

$$\|T(t) - \tilde{T}_{\text{amb}}\| \leq M_T e^{-k_T t}$$

for  $t \geq 0$ , where  $k_T = \min\{k, 1/\Theta_{\text{th}}\}$  and on the basis of (64)–(66)

$$M_T = \sqrt{n} T(0) \left( 1 + \tilde{M}_T \frac{I_0}{C_{\text{th}}} \exp \left( -\frac{g_m}{\bar{g}} \right) M_v^2 \sinh \left( \frac{V_M}{V_0} \right) \right)$$

and

$$\tilde{M}_T = \max \left\{ \frac{1}{|2k - \Theta_{\text{th}}|}, \frac{1}{ek} \right\}.$$

$\blacksquare$

## APPENDIX D

*Proposition 6:* Consider the scalar DVI

$$\dot{x}(t) \in a(t) - N_{[x_m, x_M]}(x(t))$$

for a.a.  $t \geq 0$ , where  $x \in \mathbb{R}$ ,  $x(0) \in [x_m, x_M]$  and  $a(t) : \mathbb{R} \rightarrow \mathbb{R}$  is continuous and tends to 0 exponentially, i.e., there exist  $H, \beta > 0$  such that  $|a(t)| \leq H e^{-\beta t}$ ,  $t \geq 0$ . Then,  $x(t) \rightarrow \bar{x} \in [x_m, x_M]$  exponentially, with convergence rate  $\beta$ , namely, we have  $|x(t) - \bar{x}| \leq (H/\beta) e^{-\beta t}$  for  $t \geq 0$ .

*Proof:* For a.a.  $t \geq 0$  we have  $\dot{x}(t) = 0$  if  $x(t) = x_M$  and  $a(t) \geq 0$  or  $x(t) = x_m$  and  $a(t) \leq 0$ . Otherwise, we have  $\dot{x}(t) = a(t)$ . Therefore,  $|\dot{x}(t)| \leq |a(t)| \leq H e^{-\beta t}$  for a.a.  $t \geq 0$ . Given  $t_2 \geq t_1 \geq 0$ , we have  $|x(t_2) - x(t_1)| = \left| \int_{t_1}^{t_2} \dot{x}(t) dt \right| \leq \int_{t_1}^{t_2} |\dot{x}(t)| dt \leq \int_{t_1}^{t_2} H e^{-\beta t} dt = (H/\beta) [e^{-\beta t_1} - e^{-\beta t_2}] \leq (2H/\beta) e^{-\beta t_1}$ . Hence, for any  $\epsilon > 0$  we have  $|x(t_2) - x(t_1)| \leq \epsilon$  when  $t_2 \geq t_1 \geq (1/\beta) \ln(2H/\beta\epsilon)$ . By the Cauchy criterion on limit existence (sufficiency part), there exists the  $\lim_{t \rightarrow \infty} x(t) = \bar{x}$ . Since  $x(t) \in [x_m, x_M]$  for all  $t \geq 0$  we necessarily have  $\bar{x} \in [x_m, x_M]$ . Finally, note that  $|x(t) - \bar{x}| = |x(t) - x(0) - (\bar{x} - x(0))| \leq \int_t^\infty |a(\sigma)| d\sigma \leq \int_t^\infty H e^{-\beta\sigma} d\sigma = (H/\beta) e^{-\beta t}$  for  $t \geq 0$ .  $\blacksquare$

*Proposition 7:* Consider the scalar differential equation

$$\dot{x}(t) = a(t) - b(x(t) - \bar{x})$$

for  $t \geq 0$ , where  $x, b \in \mathbb{R}$ ,  $b > 0$ , and  $a : \mathbb{R} \rightarrow \mathbb{R}$  is continuous and tends to 0 exponentially, i.e., there exist  $H, \beta > 0$  such that  $|a(t)| \leq H e^{-\beta t}$ ,  $t \geq 0$ . Then,  $x(t) \rightarrow \bar{x}$  exponentially as  $t \rightarrow \infty$  with convergence rate equal to  $\min\{b, \beta\}$  if  $b \neq \beta$  and  $b/2 = \beta/2$  if  $b = \beta$ .

*Proof:* Let  $y = x - \bar{x}$ , so that  $\dot{y}(t) = a(t) - by(t)$ . By the variation of constants formula we have

$$y(t) = e^{-bt} y(0) + e^{-bt} \int_0^t e^{b\sigma} a(\sigma) d\sigma, \quad t \geq 0.$$

If  $\beta < b$ . We have

$$\left| \int_0^t e^{b\sigma} a(\sigma) d\sigma \right| \leq \left| H \int_0^t e^{b\sigma} e^{-\beta\sigma} d\sigma \right| = \frac{H}{b - \beta} (e^{(b-\beta)t} - 1)$$

and

$$|y(t)| \leq e^{-bt} |y(0)| + \frac{H}{b - \beta} (e^{-\beta t} - e^{-bt})$$

$$\leq \left( \frac{H}{b-\beta} + |y(0)| \right) e^{-bt}, \quad t \geq 0. \quad (64)$$

Similarly, if  $b < \beta$  we obtain

$$\|y(t)\| \leq |y(0)| \left( 1 + \frac{H}{\beta-b} \right) e^{-bt}, \quad t \geq 0. \quad (65)$$

If  $b = \beta$ , we easily obtain

$$\begin{aligned} |y(t)| &\leq (|y(0)| + Ht)e^{-bt} \\ &\leq \left( |y(0)| + \frac{2H}{eb} \right) e^{-bt/2}, \quad t \geq 0. \end{aligned} \quad (66)$$

■

#### ACKNOWLEDGMENT

The authors wish to sincerely thank the Associate Editor and the anonymous reviewers for the constructive comments which helped to significantly improve the content and presentation of the article.

#### REFERENCES

- [1] M. M. Waldrop, "The chips are down for Moore's law," *Nature News*, vol. 530, no. 7589, pp. 144–147, Feb. 2016.
- [2] R. S. Williams, "What's next? [The end of Moore's law]," *Comput. Sci. Eng.*, vol. 19, no. 2, pp. 7–13, Mar. 2017.
- [3] S. Li, L. Xu, and S. Zhao, "The Internet of Things: A survey," *Inf. Syst. Frontiers*, vol. 17, no. 2, pp. 243–259, 2015.
- [4] O. Krestinskaya, A. P. James, and L. O. Chua, "Neuromemristive circuits for edge computing: A review," *IEEE Trans. Neural Netw. Learn. Syst.*, vol. 31, no. 1, pp. 4–23, Jan. 2020, doi: 10.1109/TNNLS.2019.2899262.
- [5] J. J. Yang and R. S. Williams, "Memristive devices in computing system: Promises and challenges," *ACM J. Emerg. Technol. Comput. Syst.*, vol. 9, no. 2, p. 11, May 2013.
- [6] M. A. Zidan, J. P. Strachan, and W. D. Lu, "The future of electronics based on memristive systems," *Nature Electron.*, vol. 1, no. 1, pp. 22–29, Jan. 2018.
- [7] C. Li et al., "Long short-term memory networks in memristor crossbar arrays," *Nature Mach. Intell.*, vol. 1, no. 1, pp. 49–57, Jan. 2019.
- [8] Q. Xia and J. J. Yang, "Memristive crossbar arrays for brain-inspired computing," *Nature Mater.*, vol. 18, no. 4, pp. 309–323, Apr. 2019.
- [9] D. Ielmini and G. Pedretti, "Device and circuit architectures for in-memory computing," *Adv. Intell. Syst.*, vol. 2, no. 7, Jul. 2020, Art. no. 2000040.
- [10] A. Sebastian, M. Le Gallo, R. Khaddam-Aljameh, and E. Eleftheriou, "Memory devices and applications for in-memory computing," *Nature Nanotechnol.*, vol. 15, no. 7, pp. 529–544, Jul. 2020.
- [11] L. Chua, "Memristor—The missing circuit element," *IEEE Trans. Circuit Theory*, vol. 18, no. 5, pp. 507–519, Sep. 1971.
- [12] L. O. Chua and S. M. Kang, "Memristive devices and systems," *Proc. IEEE*, vol. 64, no. 2, pp. 209–223, Feb. 1976.
- [13] P. Mazumder, S. M. Kang, and R. Waser, "Memristors: Devices, models, and applications," *Proc. IEEE*, vol. 100, no. 6, pp. 1–9, Jun. 2012.
- [14] A. Ascoli, F. Corinto, V. Senger, and R. Tetzlaff, "Memristor model comparison," *IEEE Circuits Syst. Mag.*, vol. 13, no. 2, pp. 89–105, May 2013.
- [15] B. Hajri, H. Aziza, M. M. Mansour, and A. Chehab, "RRAM device models: A comparative analysis with experimental validation," *IEEE Access*, vol. 7, pp. 168963–168980, 2019.
- [16] P. Chen and S. Yu, "Compact modeling of RRAM devices and its applications in 1T1R and 1S1R array design," *IEEE Trans. Electron Devices*, vol. 62, no. 12, pp. 4022–4028, Dec. 2015.
- [17] S. Kvatinisky, E. G. Friedman, A. Kolodny, and U. C. Weiser, "TEAM: Threshold adaptive memristor model," *IEEE Trans. Circuits Syst. I, Reg. Papers*, vol. 60, no. 1, pp. 211–221, Jan. 2013.
- [18] L. Chua, "Everything you wish to know about memristors but are afraid to ask," *Radioengineering*, vol. 24, no. 2, pp. 319–368, Jun. 2015.
- [19] F. Corinto, M. Forti, and L. O. Chua, *Nonlinear Circuits and Systems With Memristors*. Cham, Switzerland: Springer, 2021.
- [20] P. Sheridan, K.-H. Kim, S. Gaba, T. Chang, L. Chen, and W. Lu, "Device and SPICE modeling of RRAM devices," *Nanoscale*, vol. 3, no. 9, pp. 3833–3840, 2011.
- [21] X. Guan, S. Yu, and H.-S.-P. Wong, "A SPICE compact model of metal oxide resistive switching memory with variations," *IEEE Electron Device Lett.*, vol. 33, no. 10, pp. 1405–1407, Oct. 2012.
- [22] Z. Jiang, S. Yu, Y. Wu, J. H. Engel, X. Guan, and H.-S. P. Wong, "Verilog-A compact model for oxide-based resistive random access memory (RRAM)," in *Proc. Int. Conf. Simul. Semiconductor Processes Devices (SISPAD)*, Sep. 2014, pp. 41–44.
- [23] M. W. Hirsch, "Convergent activation dynamics in continuous time networks," *Neural Netw.*, vol. 2, no. 5, pp. 331–349, 1989.
- [24] M. A. Cohen and S. Grossberg, "Absolute stability of global pattern formation and parallel memory storage by competitive neural networks," *IEEE Trans. Syst., Man, Cybern.*, vol. SMC-13, no. 5, pp. 815–825, Sep./Oct. 1983.
- [25] J. J. Hopfield, "Neurons with graded response have collective computational properties like those of two-state neurons," *Proc. Nat. Acad. Sci. USA*, vol. 81, no. 10, pp. 3088–3092, May 1984.
- [26] L. O. Chua and L. Yang, "Cellular neural networks: Theory," *IEEE Trans. Circuits Syst.*, vol. 35, no. 10, pp. 1257–1272, Oct. 1988.
- [27] J. Zurada, *Introduction to Artificial Neural Systems*. Eagan, MN, USA: West Publishing, 1992.
- [28] S. Haykin, *Neural Networks: A Comprehensive Foundation*. Upper Saddle River, NJ, USA: Prentice-Hall, 1999.
- [29] L. O. Chua, "Special issue on nonlinear waves, patterns and spatio-temporal chaos in dynamic arrays," *IEEE Trans. Circuits Syst. I, Fundam. Theory Appl.*, vol. 42, no. 10, pp. 557–823, Oct. 1995.
- [30] L. O. Chua and T. Roska, *Cellular Neural Networks and Visual Computing: Foundation and Applications*. Cambridge, U.K.: Cambridge Univ. Press, 2005.
- [31] P. Liu, J. Wang, and Z. Zeng, "An overview of the stability analysis of recurrent neural networks with multiple equilibria," *IEEE Trans. Neural Netw. Learn. Syst.*, vol. 34, no. 3, pp. 1098–1111, Mar. 2023.
- [32] M. Forti and A. Tesi, "New conditions for global stability of neural networks with application to linear and quadratic programming problems," *IEEE Trans. Circuits Syst. I, Fundam. Theory Appl.*, vol. 42, no. 7, pp. 354–366, Jul. 1995.
- [33] A. N. Michel, J. A. Farrell, and W. Porod, "Qualitative analysis of neural networks," *IEEE Trans. Circuits Syst.*, vol. 36, no. 2, pp. 229–243, Feb. 1989.
- [34] S. Yang, Q. Liu, and J. Wang, "A collaborative neurodynamic approach to multiple-objective distributed optimization," *IEEE Trans. Neural Netw. Learn. Syst.*, vol. 29, no. 4, pp. 981–992, Apr. 2018.
- [35] S. Arik, "New criteria for stability of neutral-type neural networks with multiple time delays," *IEEE Trans. Neural Netw. Learn. Syst.*, vol. 31, no. 5, pp. 1504–1513, May 2020.
- [36] M. Di Marco, M. Forti, M. Grazzini, and L. Pancioni, "Limit set dichotomy and multistability for a class of cooperative neural networks with delays," *IEEE Trans. Neural Netw. Learn. Syst.*, vol. 23, no. 9, pp. 1473–1485, Sep. 2012.
- [37] M. Forti and A. Tesi, "Absolute stability of analytic neural networks: An approach based on finite trajectory length," *IEEE Trans. Circuits Syst. I, Reg. Papers*, vol. 51, no. 12, pp. 2460–2469, Dec. 2004.
- [38] M. Di Marco, M. Forti, and L. Pancioni, "Complete stability of feedback CNNs with dynamic memristors and second-order cells," *Int. J. Circuit Theory Appl.*, vol. 44, no. 11, pp. 1959–1981, Nov. 2016.
- [39] M. Di Marco, M. Forti, and L. Pancioni, "Convergence and multistability of nonsymmetric cellular neural networks with memristors," *IEEE Trans. Cybern.*, vol. 47, no. 10, pp. 2970–2983, Oct. 2017.
- [40] M. Di Marco, M. Forti, and L. Pancioni, "Memristor standard cellular neural networks computing in the flux-charge domain," *Neural Netw.*, vol. 93, pp. 152–164, Sep. 2017.
- [41] K. Deng, S. Zhu, G. Bao, J. Fu, and Z. Zeng, "Multistability of dynamic memristor delayed cellular neural networks with application to associative memories," *IEEE Trans. Neural Netw. Learn. Syst.*, vol. 34, no. 2, pp. 690–702, Feb. 2023.
- [42] K. Deng, S. Zhu, W. Dai, C. Yang, and S. Wen, "New criteria on stability of dynamic memristor delayed cellular neural networks," *IEEE Trans. Cybern.*, vol. 52, no. 6, pp. 5367–5379, Jun. 2022.
- [43] M. Yuan, W. Wang, Z. Wang, X. Luo, and J. Kurths, "Exponential synchronization of delayed memristor-based uncertain complex-valued neural networks for image protection," *IEEE Trans. Neural Netw. Learn. Syst.*, vol. 32, no. 1, pp. 151–165, Jan. 2021.

- [44] Y. Kao, Y. Li, J. H. Park, and X. Chen, "Mittag-Leffler synchronization of delayed fractional memristor neural networks via adaptive control," *IEEE Trans. Neural Netw. Learn. Syst.*, vol. 32, no. 5, pp. 2279–2284, May 2021.
- [45] X. Yang, J. Cao, and J. Liang, "Exponential synchronization of memristive neural networks with delays: Interval matrix method," *IEEE Trans. Neural Netw. Learn. Syst.*, vol. 28, no. 8, pp. 1878–1888, Aug. 2017.
- [46] X. Yang and D. W. C. Ho, "Synchronization of delayed memristive neural networks: Robust analysis approach," *IEEE Trans. Cybern.*, vol. 46, no. 12, pp. 3377–3387, Dec. 2016.
- [47] X. Yang, J. Cao, and J. Qiu, "Pth moment exponential stochastic synchronization of coupled memristor-based neural networks with mixed delays via delayed impulsive control," *Neural Netw.*, vol. 65, pp. 80–91, May 2015.
- [48] M. Di Marco, M. Forti, R. Moretti, L. Pancioni, G. Innocenti, and A. Tesi, "Convergence of a class of delayed neural networks with real memristor devices," *Mathematics*, vol. 10, no. 14, p. 2439, Jul. 2022.
- [49] R. Tetzlaff, A. Ascoli, I. Messaris, and L. O. Chua, "Theoretical foundations of memristor cellular nonlinear networks: Memcomputing with bistable-like memristors," *IEEE Trans. Circuits Syst. I, Reg. Papers*, vol. 67, no. 2, pp. 502–515, Feb. 2020.
- [50] J. P. Aubin and A. Cellina, *Differential Inclusions. Set-Valued Maps and Viability Theory*. Berlin, Germany: Springer-Verlag, 1984.
- [51] S. P. Boyd, L. El Ghaoui, E. Feron, and V. Balakrishnan, *Linear Matrix Inequalities in System and Control Theory*. Philadelphia, PA, USA: SIAM, 1994, vol. 15.
- [52] E. Kaszkurewicz and A. Bhaya, *Matrix Diagonal Stability in Systems and Computation*. Berlin, Germany: Springer, 2012.
- [53] D. Hershkowitz, "Recent directions in matrix stability," *Linear Algebra Appl.*, vol. 171, pp. 161–186, Jul. 1992.
- [54] M. Lanza et al., "Standards for the characterization of endurance in resistive switching devices," *ACS Nano*, vol. 15, no. 11, pp. 17214–17231, Nov. 2021.
- [55] J. J. Hopfield, "Neural networks and physical systems with emergent collective computational abilities," *Proc. Nat. Acad. Sci. USA*, vol. 79, no. 8, pp. 2554–2558, 1982.
- [56] Y. Nesterov and A. Nemirovsky, *Interior Point Polynomial Methods for Convex Programming: Theory and Applications*. Philadelphia, PA, USA: SIAM, 1993.
- [57] Z. Yi, P. Ann Heng, and K. Sak Leung, "Convergence analysis of cellular neural networks with unbounded delay," *IEEE Trans. Circuits Syst. I, Fundam. Theory Appl.*, vol. 48, no. 6, pp. 680–687, Jun. 2001.
- [58] N. Du et al., "Practical guide for validated memristance measurements," *Rev. Sci. Instrum.*, vol. 84, no. 2, Feb. 2013, Art. no. 023903.
- [59] A. Arbi, "Novel traveling waves solutions for nonlinear delayed dynamical neural networks with leakage term," *Chaos, Solitons Fractals*, vol. 152, Nov. 2021, Art. no. 111436.
- [60] K. Aihara, T. Takabe, and M. Toyoda, "Chaotic neural networks," *Phys. Lett. A*, vol. 144, nos. 6–7, pp. 333–340, 1990.
- [61] A. Arbi, N. Tahri, C. Jammazi, C. Huang, and J. Cao, "Almost anti-periodic solution of inertial neural networks with leakage and time-varying delays on timescales," *Circuits, Syst., Signal Process.*, vol. 41, pp. 1940–1956, Nov. 2022.
- [62] Y. Guo, S. S. Ge, and A. Arbi, "Stability of traveling waves solutions for nonlinear cellular neural networks with distributed delays," *J. Syst. Sci. Complex.*, vol. 35, no. 1, pp. 18–31, Feb. 2022.
- [63] P. Arena, R. Caponetto, L. Fortuna, and D. Porto, "Bifurcation and chaos in noninteger order cellular neural networks," *Int. J. Bifurcation Chaos*, vol. 8, no. 7, pp. 1527–1539, Jul. 1998.
- [64] A. Arbi and N. Tahri, "Almost anti-periodic solution of inertial neural networks model on time scales," in *Proc. MATEC Web Conf.*, vol. 355. Les Ulis, France: EDP Sciences, 2022, p. 02006.
- [65] M. D. Marco, M. Forti, L. Pancioni, G. Innocenti, A. Tesi, and F. Corinto, "Oscillatory circuits with a real non-volatile Stanford memristor model," *IEEE Access*, vol. 10, pp. 13650–13662, 2022.
- [66] A. Arbi, C. Aouiti, and A. Touati, "Uniform asymptotic stability and global asymptotic stability for time-delay Hopfield neural networks," in *Artificial Intelligence Applications and Innovations*. Halkidiki, Greece: Springer, Sep. 2012, pp. 483–492.
- [67] G. Setti, P. Thiran, and C. Serpico, "An approach to information propagation in 1-D cellular neural networks. II. Global propagation," *IEEE Trans. Circuits Syst. I, Fundam. Theory Appl.*, vol. 45, no. 8, pp. 790–811, Aug. 1998.
- [68] P. Thiran, K. R. Crouse, L. O. Chua, and M. Hasler, "Pattern formation properties of autonomous cellular neural networks," *IEEE Trans. Circuits Syst. I, Fundam. Theory Appl.*, vol. 42, no. 10, pp. 757–774, Oct. 1995.
- [69] M. Arcak and E. D. Sontag, "Diagonal stability of a class of cyclic systems and its connection with the secant criterion," *Automatica*, vol. 42, no. 9, pp. 1531–1537, Sep. 2006.
- [70] Z. Galias, "Simulations of memristors using the Poincaré map approach," *IEEE Trans. Circuits Syst. I, Reg. Papers*, vol. 67, no. 3, pp. 963–971, Mar. 2019.
- [71] M. Di Marco, M. Forti, M. Grazzini, and L. Pancioni, "On global exponential stability of standard and full-range CNNs," *Int. J. Circuit Theory Appl.*, vol. 36, nos. 5–6, pp. 653–680, Jul. 2008.